SECTION 0 - DOCUMENT LISTINGS

TABLE OF CONTENTS/REVISION STATUS

Each section has its own Table of Contents/Revision Status

1 INTRODUCTION

1.1 PURPOSE

This index contains a complete list of Digital Standards, A-SP-7665XXX-X-X Specifications, and other documentation under EL class control.

1.2 SCOPE

1.2.1 Section 0: Document Listings

Table 1 lists each Digital Standard in numerical (DEC STD) order with the abstract and the current revision level.

Table 2 lists all EL class manuals and specifications by document number and title. Each entry includes the revision level, and, for manuals, an abstract.

Table 3 lists each A-SP-7665XXX-X-X specification in numerical order with title and current revision level.

1.2.2 Section 1: Information Locator

The Information Locator (a separate document) contains two tables to help individuals find information:

Table 1-1 lists standards according to specific technical areas covered by Digital Standards. These areas include Design/Drafting Services, Documentation, Customer Services Systems Engineering (CSSE), Inspection/Quality Control, Hardware Engineering Design, Manuals, Manufacturing, Project Management, and Software Engineering.

Table 1-2 lists subject keywords for standards, manual, and specifications and is arranged alphanumerically to help people locate the appropriate source of information for a particular subject.

1.2.3 Section 2: Status Report

Standards and Methods Control is continously involved in the creation of new standards, specifications and manuals, and in the on-going revision process required to keep existing documentation up-to-date. Section 2 is status report that summarizes activity on new and revised standards, specifications, and manuals. In many cases, review copies of not-yet-approved documents listed in the status report can be obtained upon request.

Additional details regarding the status of current projects can be obtained by contacting the manager of Standards and Methods Control, Joe Kurta, DTN: 223-8895, or the Writing Group Supervisor, Don Mehaffey, DTN: 223-3734

1.2.4 Section 3: Standards Management Information

This section is published for the purpose of resolving standards management issues. Refer to subhead 1.3.2.

1.2.5 Section 4: Obsolete Documents

This section is used to keep track of Digital Standards and other documents that are no longer valid.

1.3 RESPONSIBILITIES

1.3.1 Standards and Methods Control

The Digital Standards Administrator in Standards and Methods Control is responsible for maintaining and publishing this index on a regular basis in accordance with DEC STD 001.

Standards and Methods Control is also responsible for the on-going effort to locate and identify individuals and organizations that are responsible for keeping the documents valid and up-to-date.

1.3.2 Standards Management

Digital Standards, and the related manuals and specifications that are part of the Digital Standards system, are not developed, maintained, implemented, or enforced by any one central authority. Instead, each standard or related document specifies its unique maintenance, implementation, and, if necessary, enforcement requirements. This is accomplished by identifying within the document a person and an organization or standing committee that accepts responsibility for the document.

The responsible person is an individual who can provide additional information on the subject and can determine that the document is up-to-date and serves its explicit purpose. Where a specific department, organization, or standing committee has responsibility for implementation and/or maintenance of a standard, that information is also included in the document. This index includes such information (when it exists) under "Resp.Person" and "Department".

2 DISTRIBUTION RESTRICTIONS

Unless otherwise specified, the documents listed in this index are classified "FOR INTERNAL USE ONLY". This means that the document is not to be distributed to non-Digital employees unless authorized by the appropriate Digital manager.

Documents that are distributed to non-Digital organizations or individuals must contain the appropriate legal notices, such as a copyright notice, proprietary information statement, etc., as specfied by DEC STD 197.

Those documents that are listed with the note "RESTRICTED DISTRIBUTION" are only distributed to individuals authorized by the person responsible for the document, per DEC STD 128.

3 STATUS AND REVISION NOTATIONS

Many of the documents listed in Tables 1, 2, and 3 include the note

"SEE STATUS REPORT FOR CURRENT ACTIVITY"

This note indicates that the document is a new document still in the process of writing or review and has not yet been approved for release or that a revision to an existing document is in process.

a. Proposed new documents in process will have revision indicators A(Xnn), where nn is the review version.

b. Proposed revisions to existing documents will have revision indicators that indicate the next sequential revision after the current released revision, such as B(Xnn).

New or revised documents that have not been approved for release are only listed in Tables 1, 2, or 3 if the originator authorizes the distribution of a review copy.

A status report of current activity is published separately in Section 2, which is to be used as a supplement to this section.

4 STANDARDS MANAGEMENT INFORMATION

Standards management reports will be published separately as Section 3 of this document. These reports will provide information pertaining to standards management issues, such as which organizations and individuals are responsible for a particular document and to which standards management domain a particular document belongs.

The standards management codes that are currently used in Section 3 are listed in the following chart:

Code	Standards Management Category
ADMN	Digital Standards Administration
CES	Component Engineering - General
CEST	Component Engineering - Test Methods
CSSE	Customer Services Systems Engineering
EID	Engineering Information and Documentation
EIDA	- CAD Systems
EIDC	- PC Design
EIDD	- Documentation
EIDE	- ECO
EIDI	- Identification
EIDM	- Micrographics
EIDP	- Process
EIDS	- Design site support, orientation, training
ESDP	Educational Services Developme and Publishing
HDA	Hardware Design Assurance
HDAB	- Industrial Packaging
HDAD	- General Design
HDAE	- Environmental
HDAM	- Electromagnetic
HDAP	- Power
HDAR	- Regulatory
HDAS	- Systems evaluation, testing
HDAT	- Telecommunications
MPA	Manufacturing Process - Assembly
MPAC	- Connectors and cables
MPAI	- Integration
MPAM	- Modules
MPAS	- Subassemblies
MPQ	Manufacturing Process Quality
MPQB	- Wirewrap/backplane
MPQC	- Calibration

TABLE 1
AB-
STRACTS
OF DEC
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Table 1. Digital Standards, Listed In Numeric Order, With Abstracts

Title: **Digital Standards System Policy**
DEC STD 001, Section 0 Revision J Date: 27-Sep-79
Abstract: Establishes the policy regarding Digital Standards, describes the categories and levels of information included in Digital Standards, and defines the responsibilities and roles assigned to the various committees and organizations involved in the management and administration of the Digital Standards System.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Creation and Change Procedures**
DEC STD 001, Section 1 Revision J Date: 27-Sep-79
Abstract: Describes procedures for the creation, revision, release, and distribution of Digital Standards.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Format and Style Requirements**
DEC STD 001, Section 02 Revision J Date: 27-Sep-79
Abstract: Describes the format and style requirements and general organization of Digital Standards.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Ac Power Wiring, Safety Grounding, Receptacle and Electrical Rating Information Requirements**
DEC STD 002 Revision C Date: 04-Dec-80
Abstract: Defines requirements for ac power wiring and grounding, types of outlets, power cords and plugs, and nameplates to be used on Digital products.

Title: **Hardware Manual Standard**
DEC STD 003 Revision D Date: 24-Jun-82
Abstract: Establishes planning, control, contents, and format requirements for the publication of all hardware manuals and hardware-related customer user guides.

Title: **Circuit Design Guidelines**
DEC STD 004 Revision A Date: 19-Jun-70
Abstract: Presents design information, rules, and formulas for use in circuit design. Includes guidelines for using active and passive components, printed circuit boards, and information about circuit performance.

Title: **Operational Alert (OPAL) Procedure**
DEC STD 005, Section 0 Revision A Date: 08-Feb-82
Abstract: Describes how an Operational Alert (OPAL) message is authorized and issued to stop shipment of a product that has a safety defect or a serious functional defect.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **In-Plant Product Hold Procedure**
DEC STD 005, Section 1 Revision A Date: 08-Feb-82
Abstract: Describes the procedure for placing a product on hold, removing a product hold, and various communication requirements.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Hardware Products Non-Compliant With Digital Standards**
DEC STD 005, Section 2 Revision A Date: 08-Feb-82
Abstract: Describes the required procedure for reporting products that do not comply with applicable Digital Standards.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Assigning Part Descriptions and Document Titles**
DEC STD 006 Revision A Date: 16-Oct-80
Abstract: Provides rules and requirements for naming parts and engineering drawings with names that are brief, consistent, and follow a uniform format. It applies to the naming of all 50-79, 94, and 95 inventory class parts and documents.

Title: **Design Review Process**
DEC STD 007 Revision C Date: 10-Nov-74
Abstract: Describes what projects require design reviews, how a design review committee is formed, when design reviews are held, and what the design review committees responsibilities are.

Title: **Project Scheduling System**
DEC STD 008 Revision A Date: 10-Nov-74
Abstract: Intended to facilitate the planning, execution, and review of development projects. All discrete projects which are expected to involve total expenditures of $10,000 or more must be included in the system. Describes scheduling techniques that are used as well as scheduling reviews.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Project Specification**
DEC STD 009 Revision A Date: 31-May-68
Abstract: Describes requirements for a product specification, including approval procedure, hardware, software, cost estimate, schedule, and design reviews.

Title: **Engineering Product Specifications - Guidelines For Generating Electrical, Physical, and Environmental Parameters.**
DEC STD 009, Section 1 Revision A Date: 25-Mar-82
Abstract: Defines hardware information that is needed by Digital personnel to accurately perform such tasks as prepare computer sites, write sales literature, and create hardware installation and operation manuals. It defines the minimum electrical, physical, and environmental parameters that must be known about a product.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Engineering Documentation Checking: Requirements**
DEC STD 010, Section 0 Revision B Date: 06-Aug-81
Abstract: Defines the responsibilities of the checker in the acceptance and release of engineering documentation. Describes what information is needed from Engineering and Design Services to support the checking process.

Title: **Engineering Documentation Checking: Document Checklist**
DEC STD 010, Section 1 Revision A Date: 06-Aug-81
Abstract: Provides a document checklist for checkers to use in meeting Digital Standards and drafting requirements.

Title: **Engineering Documentation Checking: Printed Circuit Checklist**
DEC STD 010, Section 2 Revision A Date: 03-Dec-81
Abstract: Provides a document checklist as guidelines for PC checkers to use in meeting Digital standards and drafting requirements.

Title: **Unified Numbering Code: Part and Document Identification Conventions**
DEC STD 012, Section 0 Revision F Date: 01-Oct-81
Abstract: States the general policy governing the composition and format of part and document identifiers. It shows how part and document identifiers are to be structured, with descriptions of all fields, and their uses. It also shows how to make changes to part and document identifiers and how to determine what the "top document" is for a set of documents.

Title: **Unified Numbering Code - Mnemonic Drawing Codes**
DEC STD 012, Section 1 Revision J Date: 27-Aug-81
Abstract: Defines the requirements for the assignment of Mnemonic Codes to all documentation under the scope of DEC STD 012. No code is considered valid on documentation covered by DEC STD 012 unless listed herein.

Title: **Unified Numbering Code - Class Codes For Part Identifiers and Document Identifiers**
DEC STD 012, Section 2 Revision K Date: 15-Jul-82
Abstract: Lists part identifers and document identifier class codes authorized for use within Digital. It identifies person/ organizations responsible for issuing numbers within each class. References to other Digital Standards are provided for details regarding special class code applications.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Unified Numbering Code - Packaged System Identification**
DEC STD 012, Section 3 Revision D Date: 27-Aug-81
Abstract: Standardizes the application of the Unified Numbering Code (UNC) for identification of part numbers for packaged systems marketed and sold by Digital.

Title: **Unified Numbering Code - Software Distribution Center Part Numbering Conventions**
DEC STD 012, Section 4 Revision D Date: 20 May-82
Abstract: Standardizes the application of the Unified Numbering Code (UNC) for identification of part numbers assigned and controlled by the Software Distribution Center.

Title: **Unified Numbering Code - Manufacturing Control Part Numbering Conventions**
DEC STD 012, Section 5 Revision B Date: 15-Jul-82
Abstract: Establishes the procedure for assigning Unified Numbering Code (UNC) part numbers by Manufacturing to permit greater flexibility in measuring and controlling material and process flow.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Unified Numbering Code - Computer Special Systems Part Numbering Conventions**
DEC STD 012, Section 6 Revision B Date: 04-Mar-82
Abstract: Describes the application of the Unified Numbering Code (UNC) for identification of part numbers assigned and controlled by Computer Special Systems (CSS).

Title: **Unified Numbering Code: 74 Class Part Numbering Conventions and Assignment Procedures**
DEC STD 012, Section 7 Revision A Date: 19-Feb-81
Abstract: Defines the requirements for the assignment and control of 74 class part identifiers.

Title: **Unified Numbering Code - Field Service Part Numbering Conventions and Assignment Procedures**
DEC STD 012, Section 8 Revision A Date: 27-Aug-81
Abstract: Defines the requirements for assignment and control of Field Service part identifiers.

Title: **Unified Numbering Code - 94 Class Tool Numbering Conventions and Assignment Procedures**
DEC STD 012, Section 9 Revision A Date: 27-Aug-81
Abstract: Defines the requirements for the assignment and control of 94 class tooling part identifiers.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Abbreviations and Units of Measurement**
DEC STD 015 Revision B Date: 13-Jan-77
Abstract: Requires that documentation for commerce in European Economic Community (EEC) use SI (metric) units of measurement and unit symbols for all quantities. This standard also provides abbreviations for use on engineering drawings. Conversion factors for commonly used USA customary units are provided.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Printed-Wiring Manufacturing Terminology**
DEC STD 016 Revision A Date: 05-Nov-81
Abstract: Establishes terms and definitions for consistent usage of printed wiring terms in engineering and manufacturing documents.

Title: **Digital Quality Policy**
DEC STD 017 Revision A(X02) Date: 27-Jan-82
Abstract: Summarizes all of Digital's design and manufacturing policies that relate to the quality of products. Also provides the basis for detailed documents in each of the subject areas covered.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Casting Standard**
DEC STD 020 Revision A Date: 09-Oct-72
Abstract: Establishes rules and design guides to be used in the preparation of drawings to define machined castings.

Title: **Cable and Harness Documentation: Part Identification Requirements**
DEC STD 022, Section 0 Revision D Date: 18-Dec-80
Abstract: Defines the part and numbering system for cables and harnesses.

Title: **Cable and Harness Documentation: Drawing Requirements**
DEC STD 022, Section 1 Revision A Date: 18-Sep-80
Abstract: Defines the drawing requirements for cable and harness design/assembly documentation.

Title: **Drawing Directory Requirements - DRB 106A, DRB 107, and DRB 108A Formats**
DEC STD 024, Section 0 Revision C Date: 18-Sep-80
Abstract: Describes the drawing directory used to list all drawings and variations required to manufacture a unit or option.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Drawing Directory Requirements - DRB 126A Format**
DEC STD 024, Section 1 Revision B Date: 24-Jun-82
Abstract: Defines the information content requirements for drawing directory format DRB 126B, which is used to list all drawings and documentation required to manufacture modules.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Parts Lists - General Requirements**
DEC STD 025, Section 0 Revision C Date: 26-Mar-81
Abstract: Establishes the information content and format for parts lists used in the design and manufacture of Digital hardware products. The general requirements are provided for both manual and automated parts lists.

Title: **Manual Parts Lists**
DEC STD 025, Section 1 Revision B Date: 18-Sep-80
Abstract: Provides detailed information requirements for manual parts lists.

Title: **Automated Parts Lists**
DEC STD 025, Section 2 Revision B Date: 18-Sep-80
Abstract: Provides detailed information requirements for automated parts lists.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Documentation Requirements and Process For Internally-Designed Hybrid Assemblies**
DEC STD 026 Revision A Date: 5-Nov-81
Abstract: Defines the requirements and process for engineering sign-off and control of an internally-designed hybrid assembly and it's related substrate.

Title: **Phase Review Policy**
DEC STD 028 Revision A Date: 03-Dec-81
Abstract: Defines the structure of the Phase Review Process for both hardware and software products. It names the phases, establishes phase exit criteria, identifies a minimum set of milestones within each phase, addresses phase transition meetings and identifies reference information.

Title: **Graphic COM System: Requirements and Procedures**
DEC STD 029 Revision A Date: 03-Dec-81
Abstract: Defines requirements and procedures for processing released computerized design information on graphic computer output microfilm (COM).

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: Module Manufacturing Standard
DEC STD 030 Revision J Date: 15-Apr-82
Abstract: Describes the module manufacturing capability of Digital and the circuit layout standards and procedures which allow that capability to be optimized. Contains all the rules that ensure the circuit design engineer a fast and economical module, and Product Line Manager volume deliveries during the production life of his/her product.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Product Serialization
DEC STD 031, Section 0 Revision E Date: 15-Jul-82
Abstract: Defines the requirements for product serialization and provides a uniform serial numbering scheme and format for Digital products.

Title: Product Model Changes
DEC STD 031, Section 1 Revision B Date: 15-Jul-82
Abstract: Describes how to label products that are modified after the product serial tag has been applied and before shipment to a customer.

Title: Site Codes
DEC STD 031, Section 2 Revision B Date: 15-Jul-82
Abstract: Specifies how to derive 2-character site codes from the 3-character codes defined in the Digital Facility Address Directory. This is to accommodate the existing processes, stamps, tags, systems, forms and documents that are used by manufacturing and engineering.

Title: VAX Architecture Standard
DEC STD 032 Revision A Date: 10-Jul-80
Abstract: Provides a definition of the VAX architecture. Provides a complete description of the VAX central processor hardware as seen by machine language programs.

Title: Microfilm Aperture Cards - Creation and Distribution Process
DEC STD 033, Section 0 Revision B Date: 23-Oct-81
Abstract: Describes microfilm aperture card creation and distribution process for engineering documentation. It also defines the format and quality requirements for microfilm aperture cards, and provides the procedures for establishing and maintaining a Microfilm Reference Library.

Title: Microfilm Aperture Cards - Requirements
DEC STD 033, Section 1 Revision B Date: 23-Oct-81
Abstract: Defines the format and quality requirements for microfilm aperture cards of engineering documentation.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Microfilm Reference Library Setup and Maintenance Procedures**
DEC STD 033, Section 2 Revision A Date: 10-Apr-80
Abstract: Provides procedures for establishing a Microfilm Reference Library for microfilm aperture cards.

Title: **Hardware Manual Covers: Content and Format Requirements**
DEC STD 035 Revision A Date: 15-Oct-81
Abstract: This standard defines the content and format requirements for the front and back covers and the spines of 8-1/2 by 11 inch manuals and user guides identified and developed under DEC STD 003, Hardware Manual Standard.

Title: **Systems Evaluation Engineering Requirements - General**
DEC STD 038, Section 0 Revision A(X02) Date: 01-Apr-82
Abstract: This level 2 standard describes the procedure followed by Systems Evaluation Engineering in the system-level testing of Digital's products. A systems evaluation considers the interactions between a software or hardware product and its environment. This document describes the evaluations performed within LSI-11, PDP-11, and VAX-11 environments.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Systems Evaluation Engineering Requirements - Software**
DEC STD 038, Section 1 Revision A(X02) Date: 01-Apr-82
Abstract: This section describes the requirements for an evaluation of system software. The goals, procedures, and success criteria for each evaluation are included.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Systems Evaluation Engineering Requirements - Hardware**
DEC STD 038, Section 2 Revision A(X02) Date: 01-Apr-82
Abstract: This section describes the requirements for an evaluation of a hardware system. The goals, procedures, and success criteria for each evaluation are included.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Customer Shipping Lists: Requirements**
DEC STD 039 Revision A(X02) Date: 19-Mar-82
Abstract: This standard defines the format and content requirements for Customer Shipping Lists.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Fine Line Board Process Requirements**
DEC STD 040 Revision A(X02) Date: 22-Feb-82
Abstract: Describes the manufacturing procedures for fine line boards, with emphasis on quality testing to produce a high fresh lot yield.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Customer Installability - Product Requirements**
DEC STD 041 Revision A Date: 24-Jun-82
Abstract: Describes product design and process requirements for hardware and software products that are to be installed by the customer.

Title: **Hardware Installation Manuals For Customer-Installable Systems**
DEC STD 042 Revision A(X02) Date: 29-Apr-82
Abstract: Establishes requirements for customer hardware installation manuals for customer-installable systems. Defines the responsibilities of each group associated with the development of such manuals, explains conformance, defines target audience, outlines manual objectives, and discusses style, manual content and production requirements.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Corporate Supplier Packaging Standard**
DEC STD 043, Section 0 Revision A(X04) Date: 10-Jul-82
Abstract: Describes Digital's material handling and packaging requirements in terms of cartons and pallets, and their size, weight, labeling, and environmental protection as they apply to shipments from suppliers and between plants.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Corporate Supplier Packaging Standard - Sheet, Blanks, Or Coils Of Steel Or Aluminum**
DEC STD 043, Section 1 Revision A(X03) Date: 15-Jun-82
Abstract: Specifies Digital's requirements for the packaging of steel and aluminum sheet, blanks, and coils.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Package Assembly Documentation Requirements**
DEC STD 044 Revision 0 Date: 15-Jul-82
Abstract: Defines the documentation requirements and process for product Package Assembly (PA) documentation.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Package Engineering Design and Test Requirements**
DEC STD 045 Revision A(X00) Date: 15-Jul-82
Abstract: Describes the mechanical tests to which Digital products and distribution packages will be subjected. It defines the intentions, responsibilities, conformance and use of the standard relative to the Phase Review Process. Included in this standard are the specific requirements and procedures for fragility tests on products and mechanical tests on distribution packages.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

DEC STD 052, Section 1 (continued)
CCITT recommendation V.28. This standard also covers manual and automatic disconnection of the DTE at the end of a call. The operational characteristics also apply to many cases where the electrical interface does not conform to RS-232-C or V.28: for example, a modem integral to a terminal.

Title: Electrical Requirements For Binary Interfaces That Conform To EIA RS-232-C or CCITT V.28
DEC STD 053 Revision A(X03) Date: 13-Jun-80
Abstract: Defines the minimum electrical interface requirements for the drivers, receivers, and interconnecting cable used to connect DTEs to DCEs and modems in accordance with EIA RS-232-C or CCITT V.28.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Purchase Specifications: Guidelines
DEC STD 055 Revision B Date: 24-May-79
Abstract: Establishes the general instructions and responsibilities for the preparation and control of Digital Purchase Specifications.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Logic Symbology - Circuit Schematic Requirements
DEC STD 056, Section 0 Revision C Date: 27-Jun-80
Abstract: Establishes the format and requirements for Logic Symbology used by Digital Equipment Corporation including the requirements for schematic logic diagrams, and the composition and form of symbols. This section also establishes general guidelines for a Logic Symbology Handbook.

Title: Symbology - Distinctive Shape Logic Symbols
DEC STD 056, Section 1 Revision C Date: 27-Jun-80
Abstract: Provides detailed requirements for the use of distinctive-shape logic symbols in schematic logic diagrams so that logic functions may be understood directly from either the shape of the symbol or the notation within the symbol.

Title: Complex (Uniform-Shape) Logic Symbols
DEC STD 056, Section 2 Revision C Date: 27-Jun-80
Abstract: Provides detailed requirements for the use of complex (uniform-shape) logic symbols in schematic logic diagrams.

Title: Discrete Electronic and Electromechanical Component Symbols
DEC STD 056, Section 3 Revision C Date: 27-Jun-80
Abstract: Provides detailed requirements for representing discrete electrical-mechanical components on schematic logic diagrams.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Electrical Interconnections Between Graphic Symbols**
DEC STD 056, Section 4 Revision C Date: 27-Jun-80
Abstract: Specifies the requirements for electrical connections between logic symbols, and provides rules for the use of signal mnemonics in the connections.

Title: **Symbology - Waivers**
DEC STD 056, Section 5 Revision C Date: 27-Jun-80
Abstract: Establishes the procedures and requirements for obtaining waivers and exceptions to this standard.

Title: **Symbology - Glossary of Terms**
DEC STD 056, Section 6 Revision C Date: 27-Jun-80
Abstract: Provides definitions for certain terms used in DEC STD 056.

Title: **Symbology - Current Logic Function Labels and Current Pin Label Definitions**
DEC STD 056, Section 7 Revision C Date: 27-Jun-80
Abstract: Provides a list of current logic function labels and pin label definitions.

Title: **Incoming Inspection Procedures: General Policy**
DEC STD 059, Section 0 Revision B Date: 23-Jul-81
Abstract: Establishes the general policy regarding requirements and responsibilities for Incoming Inspection Procedures.

Title: **PAVES Incoming Inspection Documentation Requirements**
DEC STD 059, Section 1 Revision B Date: 23-Jul-81
Abstract: Describes requirements for the Incoming Inspection documentation on the Part Analysis Vendor Evaluation System (PAVES).

Title: **Incoming Inspection Procedures - Metal Fabrication And Plastics**
DEC STD 059, Section 2 Revision B Date: 23-Jul-81
Abstract: Establishes a uniform method for generating, controlling, and distributing Incoming Inspection Procedures (II's) for metal fabrication and plastics.

Title: **Incoming Inspection: Standard Operating Procedures**
DEC STD 059, Section 3 Revision B Date: 23-Jul-81
Abstract: Establishes the minimum requirements for documenting standard operating procedures for Incoming Inspection areas. Defines flow of materials and forms, methods of identification and traceability, methods to control measuring equipment, and the required quality documentation for Incoming Inspection areas.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: Design and Certification Of Hardware Products To National and International Regulations and Standards
DEC STD 060, Section 0 Revision B Date: 16-Oct-80
Abstract: Defines the intentions, responsibilities and controls for designing and certifying Digital hardware products to meet the requirements of nationally - and internationally - recognized organizations.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Design and Certification of Hardware Products To National and International Regulations and Standards - Specific Requirements
DEC STD 060, Section 1 Revision C Date: 18-Mar-82
Abstract: Lists the specific Digital standards and external regulations and standards that apply to Digital's hardware product designs. It also lists requirements that have been investigated and found to be not applicable to Digital's hardware product designs.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Product Submittal To U.S. and Foreign Agencies
DEC STD 062 Revision A Date: 05-Nov-81
Abstract: Defines the various agencies around the world to which our products must be submitted. This standard also identifies the Digital people in various countries who submit our products to these agencies, the submittal procedures, and legally-mandated labeling procedures.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Handling of Electrostatic Sensitive Devices - Procedures and Protective Materials
DEC STD 067 Revision A Date: 17-Sep-81
Abstract: Specifies the proper methods of handling components that are sensitive to electrostatic discharge during assembly, test and field repair or retrofit. Special materials and handling methods to be used during manufacture and shipping are listed and described.

Title: Finish and Color Standard - Introduction and General Requirements
DEC STD 092, Section 0 Revision E Date: 05-Nov-81
Abstract: This section describes the content of sections 1 through 5, and the purpose and use of each section.

Title: Finish and Color Standard - Finish Standard for Applications
DEC STD 092, Section 1 Revision E Date: 05-Nov-81
Abstract: This section describes the Digital finish numbering system, and is intended for those who will apply the finish to manufactured parts.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Finish and Color Standard - Finish Material Standard for Suppliers**
DEC STD 092, Section 2 Revision E Date: 05-Nov-81
Abstract: This section defines the procedure to be followed, and the requirements to be met, by finish material suppliers.

Title: **Finish and Color Standard - Finish Material Test Requirements**
DEC STD 092, Section 3 Revision E Date: 05-Nov-81
Abstract: This section defines the test requirements and test methods applied to the finishes used for Digital parts.

Title: **Finish and Color Standard - Approved Finish Specifications**
DEC STD 092, Section 4 Revision E Date: 05-Nov-81
Abstract: This section contains a complete list of specifications for finishes approved for use on Digital products.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Finish and Color Standard - Digital Color List**
DEC STD 092, Section 5 Revision E Date: 05-Nov-81
Abstract: This section contains a list of currently approved Digital colors and color identification numbers.

Title: **Finish and Color Standard - Digital Approved Paint Suppliers and Material Identification**
DEC STD 092, Section 6 Revision A(X03) Date: 26-May-82
Abstract: This section is a guide to the procurement of protective and decorative industrial coatings and paints used for production applications on Digital-approved substrates.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Finish and Color Standard - Plastic Color Control and Material Identification**
DEC STD 092, Section 7 Revision A(X03) Date: 26-May-82
Abstract: Defines guidelines to ensure that color-impregnated plastics used by Digital exhibit acceptable color drift characteristics for product uniformity; establishes a Digital color control source to initially evaluate, approve, and document all plastic colors to ensure color conformity and avoid delays and mismatching from alternate sources; and ensures that flame-retardant properties are not degraded in the process of obtaining new plastic color formulations.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Finish Specification 092-A10X-XXX: Smooth Paint Finish**
092-04-A10X Revision A Date: 05-Nov-81

Title: **Finish Specification 092-A11X-XXX: Smooth Paint Finish**
092-04-A11X Revision A Date: 05-Nov-81

Table 1. Digital Standards, Listed In Numeric Order,
With Abstracts (Continued)

Title: Finish Specification 092-A12X-XXX: Smooth Paint Finish
092-04-A12X Revision A Date: 05-Nov-81

Title: Finish Specification 092-A13X-XXX Texture Paint Finish
092-04-A13X Revision A Date: 05-Nov-81

Title: Finish Specification 092-A14X-XXX Textu·e Paint Finigh
092-04-A14X Revision A Date: 05-Nov-81

Title: Finish Specification 092A15X-XXX Texture Paint Finish
092-04-A15X Revision A Date: 05-Nov-81

Title: Finish Specification 092-A16X-XX Marking Paint Finish
092-04-A16X Revision A Date: 05-Nov-81

Title: Finish Specification 092-A17X-XXX Clear Hard Coat Finish
092-04-A17X Revision A Date: 05-Nov-81

Title: Finish Specification 092-A40X-XXX Surface Preparation Finish
092-04-A40X Revision A Date: 05-Nov-81

Title: Finish Specification 092A41X-XXX Texture Paint Finish for
Plastic Covers
092-04-A41X Revision A Date: 05-Nov-81

Title: Finish Specification 092-A45X-XXX Cabinet Interior
Protective Finish
092-04-A45X Revision A Date: 05-Nov-81

Title: Finish Specification 092-A46X-XXX Nylon Coating
092-04-A46X Revision A Date: 05-Nov-81

Title: Finish Specification 092-A60X-XXX Urethane Coating For
Magnets
092-04-A60X Revision A Date: 05-Nov-81

Title: Finish Specification A092-B05X-XXX Zinc Plate With Yellow
Chromate
092-04-B05X Revision A Date: 05-Nov-81

Title: Finish Specification 092-B06X-XXX Zinc Plate With Yellow
Chromate
092-04-B06X Revision A Date: 05-Nov-81

Title: Finish Specification 092-B08X-XXX Zinc Plate With Clear
Chromate
092-04-B08X Revision A Date: 05-Nov-81

Title: Finish Specification 092-B09X-XXX Bright Cadmium Plate
092-04-B09X Revision A Date: 05-Nov-81

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Engineering Change Orders (ECO) - Engineering ECO Coordination Procedure**
DEC STD 100, Section 1A Revision B Date: 17-Jun-81
Abstract: Defines the procedure used by an Engineering ECO Coordinator to support the ECO process for Digital hardware. The procedure for the overall process is given, together with a method of handling supplement ECOs and for validating or assisting in determining which engineers are responsible for a specific ECO process.

Title: **Hardware ECO Form Procedure**
DEC STD 100, Section 1B Revision A Date: 07-Apr-81
Abstract: Defines procedure for filling out ECO face sheet and associated forms required for hardware ECO.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Engineering Change Orders (ECO) - Financing ECOs To Hardware**
DEC STD 100, Section 1C Revision F Date: 03-May-82
Abstract: Describes the policy and procedure for financing ECOs to hardware.

Title: **Engineering Change Orders - Purchase Specifications**
DEC STD 100, Section 2 Revision F Date: 20-May-82
Abstract: Describes the policy and procedure for changing purchase specifications. Also specifies responsibilities and roles assigned to various individuals and organizations involved in the purchase specification ECO process.

Title: **Diagnostic Engineering Change Orders and Patch Orders (DECO's and DEPO's)**
DEC STD 100, Section 3 Revision F Date: 14-May-81
Abstract: Describes the policies and procedures for the administration and control of diagnostic engineering change orders, patch orders, and submissions of new diagnostic products to the Software Distribution Center.

Title: **Diagnostic Engineering DECO/DEPO Submission Form Procedure**
DEC STD 100, Section 3A Revision A Date: 14-May-81
Abstract: Defines the procedure used by Diagnostic Engineers to fill out the DECO/DEPO Submission form.

Title: **Manufacturing Operations Plan for Assembly, Inspection, and Test: Policy and Requirements**
DEC STD 101 Revision D Date: 14-May-81
Abstract: Presents a policy for the structure of a Manufacturing Operations Plan for all product lines and businesses within Digital Equipment Corporation. This Manufacturing Operations Plan allows product lines and businesses the flexibility to assure that controls are implemented so all products are produced in conformance to specifications.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Environmental Standard for Computers and Peripherals - General Test Requirements**
DEC STD 102, Section 0 Revision D Date: 18-Mar-82
Abstract: Defines the environmental conditions to which products marketed by Digital Equipment Corporation must conform before being considered acceptable for product announcement.

Title: **Temperature, Humidity, and Altitude Test Requirements**
DEC STD 102, Section 1 Revision D Date: 18-Mar-82
Abstract: Defines environmental classifications and establishes test requirements used to assure that hardware products meet temperature, humidity, and altitude requirements.

Title: **Mechanical Shock and Vibration Test Requirements**
DEC STD 102, Section 2 Revision D Date: 18-Mar-82
Abstract: Establishes the levels of mechanical shock and vibration that hardware products must be able to withstand.

Title: **Physical Stability Requirements During Shipping and Handling**
DEC STD 102, Section 3 Revision D Date: 18-Mar-82
Abstract: States the physical stability requirements for hardware products that are designed to be free-standing when installed. Two product states are considered: the product as installed and the product as shipped.

Title: **Acoustic Noise Test Requirements**
DEC STD 102, Section 4 Revision A(X01) Date: 01-Sep-81
Abstract: Specifies uniform procedures for measuring and reporting acoustic noise emission.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **EMI/Electromagnetic Interface**
DEC STD 102, Section 7 Revision B Date: 09-Nov-78
Abstract: Defines the electromagnetic environment that Digital products can be expected to be subjected to and define the limits of the electromagnetic interface that these devices are allowed to produce.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Electromagnetic Compatibility (EMC) Hardware Design Requirements**
DEC STD 103, Section 0 Revision A(X01) Date: 16-Jun-82
Abstract: This level 1 standard defines the limits of electromagnetic interaction between any of Digital's hardware products and it's environment. It outlines measurement methodology, acceptability criteria, and responsibilities. Section 0 is an overview.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: FCC Labeling And User Manual Information
DEC STD 103, Section 1 Revision A Date: 18-Dec-80
Abstract: Provides an overview of the process for labeling equipment and modifying user manuals in response to FCC regulations cited in FCC Rules Part 15.J.

Title: FCC Non-Compliance Labeling
DEC STD 103, Section 1A Revision B Date: 2-Nov-81
Abstract: Describes the policy for labeling applicable Digital equipment that has not been verified or certified as complying with FCC regulations cited in FCC Rules, Part 15.J.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: FCC Non-Verification Labeling
DEC STD 103, Section 1A Revision C(X00) Date: 16-Jun-82
Abstract: Describes the policy for labeling applicable Digital Equipment that has not been verified or certified as complying with FCC regulations cited in FCC Part 15.J. Because non-verification labeling will only be required and permitted for a certain time period and under certain circumstances, this section of DEC STD 103 is expected to be voided and not applicable to most Digital products in the near future.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: FCC Compliance Labeling and User Information
DEC STD 103, Section 1B Revision A(X02) Date: 16-Jun-82
Abstract: Describes the policy for labeling applicable Digital equipment that has been verified or certified as complying with FCC regulations cited in FCC Rules, Part 15.J.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: FCC Compliance Equipment User Information
DEC STD 103, Section 1C Revision A(X00) Date: 01-Oct-81
Abstract: Describes the policy for associating user information with Digital equipment that has been verified or certified as complying with FCC regulations cited in FCC Rules, Part 15.J.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: FCC Certification Approval Process
DEC STD 103, Section 1D Revision A(X00) Date: 04-Jan-82
Abstract: Describes the process for verifying or certifying Digital equipment as complying with FCC regulations cited in FCC Rules, Part 15.J.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Electromagnetic Interference (EMI) Control**
DEC STD 103, Section 2 Revision A(X01) Date: 16-Jun-82
Abstract: Defines the internal Digital requirements corresponding with external, legal requirements about EMI, the test methodology, and the internal approval process.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Electromagnetic Susceptibility (EMS):Immunity Control**
DEC STD 103, Section 3 Revision A(X01) Date: 16-Jun-82
Abstract: Defines realistic electromagnetic immunity levels against the effects of radio and TV transmitters, as well as other electromagnetic spectrum pollution from industrial sources.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Electrostatic Discharge (ESD) Control**
DEC STD 103, Section 4 Revision A(X01) Date: 16-Jun-82
Abstract: Defines the test configurations and acceptability criteria for ESD control of EDP and office equipment.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Product Acoustic Noise Acceptability**
DEC STD 104 Revision A(X04) Date: 14-JUL-82
Abstract: Defines acceptability criteria for acoustic noise emitted from digital products and groups of products.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Display Workstation Ergonomics (Human Factors): Desig Criteria**
DEC STD 105 Revision A Date: 17-Dec-81
Abstract: Provides design criteria and recommendations to produce display workstations that safeguard the comfort and well-being of operators.

Title: **Standard for In-House Acceptance Procedures**
DEC STD 106 Revision A Date: 10-Dec-73
Abstract: Outlines the general steps to be followed in creating an acceptance procedure for all systems and options manufactured by Digital. Included are: computers, computer options, special systems, interfaces, etc.

Title: **Digital Standard For Terminal Keyboards Standard Keyboard Layouts**
DEC STD 107, Section 0 Revision B Date: 03-Jan-80
Abstract: Defines requirements for keyboard layouts, keyboard codes, and key pads to be used for all terminal designs that are introduced into production after January 1, 1978.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Digital Standard For Terminal Keyboards Registry Of Graphic Character Sets**
DEC STD 107, Section 1 Revision B Date: 06-Aug-81
Abstract: Defines the graphic character sets to be used for Digital hardware and software products for information interchange. The definitions include code generated by each graphic character.

Title: **DEC Standard for Escape Sequence**
DEC STD 110 Revision B Date: 07-Mar-75
Abstract: Indiscriminate echoing of ESC as 33_8 is prohibited. Where it is desirable to print some displayable character to provide visible confirmation that ESC has been received by the program, then that character must be single dollar sign ($\$;(44)_8$). ESC is the character which initially delimits an ESC sequence and ESC may carry no other meaning, even though ESC currently has many other meanings. Applies to all new DEC terminals.

Title: **DEC Standard for Terminal Synchronization**
DEC STD 111 Revision A Date: 06-Mar-75
Abstract: DC1 and DC3, 21_8 and 23_8, (formerly XON and XOFF) respectively, are to be used for synchronization of terminal keyboards in the manner described in the standard DC2 and DC4, 22_8 and 24_8 formerly TAPE and NOT-TAPE respectively, are reserved for future use, likely for synchronization as well.

Title: **Standard Date Format for Output**
DEC STD 112 Revision B Date: 10-Feb-77
Abstract: This standard ensures an unambiguous interpretation of dates by readers around the world. This format is one which is in common use throughout most of the world, is reasonably terse, is well human engineered and is easy to produce in any computer system.

Title: **Metric Dimensioning on Engineering Drawings - General Requirements**
DEC STD 114 Revision A Date: 24-Aug-80
Abstract: Presents requirements for converting from the inch to the metric system while maintaining interchangeability.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Engineering Drawing Requirements - Industry Standards Adopted By Digital Engineering and Manufacturing Documentation Organizations**
DEC STD 114, Section 0 Revision B(X00) Date: 01-Apr-82
Abstract: Defines the Industry Standards and Company Unique requirements for Engineering Documentation Practices within Digital.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Digital Product Safety - Test Procedures**
DEC STD 119, Section 2 Revision D Date: 12-Nov-81
Abstract: Presents test procedures required to determine if products meet design criteria.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Digital Product Safety - AS C100 Requirements**
DEC STD 119, Section 3 Revision D Date: 12-Nov-81
Abstract: Presents special Australian Standards that apply to products to be sold in Australia.

Title: **Digital Product Safety - Reporting Product Safety Incidents In Digital Plants**
DEC STD 119, Section 5 Revision A(X00) Date: 08-Apr-82
Abstract: This standard defines product safety incidents, describes procedures to report such incidents, and provides a form with instructions for reporting product safety incidents.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Cooling Standard**
DEC STD 120 Revision A Date: 06-Mar-75
Abstract: A quick reference to which a Design Engineer can refer for questions on cooling conventional circuit boards. There are also included some general guidelines for cabinets and component level thermal calculations to enable the Engineer to estimate the cooling required for this system.

Title: **Digital Data Communications Message Protocol (DDCMP)**
DEC STD 121, Section 0 Revision A Date: 30-Mar-78
Abstract: Describes the functions, characteristics, interfaces, message formats, and operation of the DDCMP protocol. It is primarily intended to assist the individual implementing DDCMP. It is structured to also provide general information describing the protocol to others who may need this level of information. It is not intended to instruct those unfamiliar with the basic principles of data communications.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Ac Power Line Standards: Design Requirements and Design Guidelines**
DEC STD 122 Revision D Date: 17-Dec-81
Abstract: Provides design requirements and guidelines for power supplies, power control equipment, and other devices that operate off primary ac power sources.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: Power Control Bus Standard
DEC STD 123 Revision A Date: 29-Apr-76
Abstract: Defines the Digital power control bus function, electrical and hardware. Hardware designed and tested to the limits stipulated may be interfaced with any other equipment complying with this standard. All hardware released following the issue date that interfaces with the power control bus must comply with this standard.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Format Standard for Manuals Produced on Typeset Media
DEC STD 124 Revision A Date: 05-Oct-78
Abstract: For personnel who are involved in preparing hardware related product literature for typeset media. It does not apply to software documentation. It must be used for any typeset manuals to be published on microfiche. This standard governs formatting procedures only.

Title: Cassette Format Standard for Labeled and Unlabeled Files
DEC STD 125 Revision B Date: 18-Jun-81
Abstract: Describes the format and labeling conventions for files, physical blocks, logical records and data written on Digital Equipment cassettes. It also describes the unlabeled standard. This standard must be followed when reading and writing cassettes intended for interchange between systems; it is recommended for other cassettes.

Title: Packaged Systems Documentation Structure
DEC STD 126 Revision A Date: 12-Apr-79
Abstract: Describes the minimum engineering drawings and documents that are required to document packaged systems.

Title: Test Methods For Semiconductor Devices, Thermal Resistance Method 1
DEC STD 127 Revision A(X00) Date: 28-Dec-81
Abstract: Defines the method to be used to measure the thermal performance of microelectronic package configurations when operated under forced convection cooling conditions.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Confidential Engineering Information and Documentation: Policy and Requirements
DEC STD 128 Revision A Date: 04-Sep-80
Abstract: Defines Digital policy and requirements for classifying, labelling, storing, and distributing documentation classified as "Restricted Distribution" or "For Internal Use Only".

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: Software Box Requirements and Procedures
DEC STD 129 Revision A Date: 08-Jan-81
Abstract: Establishes the requirements for content, identification, creation, and quality control of software boxes.

Title: Product/System Business Plans: Content Requirements and Format Guidelines
DEC STD 130 Revision B Date: 03-Dec-81
Abstract: Business plans shall be written for all new products, except when deemed unnecessary by the Product Engineering Group (PEG). This standard describes content requirements for a business plan. It applies to all new products being considered for development. It outlines requirements for the Executive Summary, System Description, Forecast, Assumptions, and Financial Analysis. A sample business plan with a recommended entries is provided.

Title: Integrated Circuit Documentation and Test System Control
DEC STD 133 Revision A Date: 10-Jun-76
Abstract: Section 1 Includes purpose, scope, and detailed descriptions of documentation and overall system. Section 2 Includes procedures for new ICs and revising documentation, and test-software and test-hardware associated with existing ICs. Section 3 Includes responsibilities for general operation, introduction of new ICs, ECO's to existing ICs, and introduction of new IC testers. Also includes an index of relevant engineering notes.

Title: Master Parts File Definitions
DEC STD 137 Revision A Date: 08-Aug-76
Abstract: Applies to persons involved with internal DEC business programming application. It describes the field formats initially developed and includes an index of relevant engineering notes.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Reliability Prediction
DEC STD 139 Revision A Date: 22-Jan-76
Abstract: Establishes MIL HBK 217B as the official Reliability Prediction technique to be used by DEC and establishes the responsibility for maintaining key parameters to assure consistent interpretations throughout the corporation.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Module Documentation Structure: Basic Requirements
DEC STD 140, Section 0 Revision D Date: 24-Jun-82
Abstract: Describes the documentation structure required to accomodate and control the release of modules, 54-class assemblies, and printed circuit (50-class) boards.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Module Documentation Structure: Wire Adds and Etch Cuts Requirements**
DEC STD 140, Section 1 Revision C Date: 24-Jun-82
Abstract: Specifies the additional documentation required to describe modules revised by wire adds and etch cuts.

Title: **Module Documentation Structure - Wire Ink Revisions**
DEC STD 140, Section 2 Revision A Date: 24-May-79
Abstract: Specifies the additional documentation required to describe modules revised by means of wire ink.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Engineering Notebook Policy and Requirements**
DEC STD 141 Revision B Date: 27-Aug-81
Abstract: Defines Digital policy and requirements for issuance, use, control, and retention of Engineering Notebooks for the purposes of capturing and retaining essential information.

Title: **Etch Board and Module Release Verification Requirements and Procedures - Manufacturing Production Release**
DEC STD 142, Section 0 Revision E Date: 26-Feb-81
Abstract: Describes the etch board (50 level), module (54-level), and parallel (50/54-level) release processes. Lists documentation items in various release package required to meet the acceptance requirements for manufacturing PC boards and modules.

Title: **Etch Board And Module Release Verification Requirements - Prototype Process**
DEC STD 142, Section 1 Revision B Date: 20-May-82
Abstract: Describes the prototype process and the interface between Engineering and Manufacturing.

Title: **Etch Board And Module Release Requirements and Procedures - Engineering Supervised Build (ESB) Process**
DEC STD 142, Section 2 Revision A Date: 26-Feb-81
Abstract: Describes the "sign-off" process for engineering-supervised build PC boards (formerly called low volume process). Defines interaction between Engineering and Manufacturing that applies to all Digital design engineering sites.

Title: **Standard for Updating Hardware/Software Manuals**
DEC STD 143 Revision A Date: 19-Aug-76
Abstract: Defines the format in which document updates are to be published.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: Disk Standard for Recording and Handling Manufacturing Detected Bad Sectors
DEC STD 144 Revision B Date: 18-Nov-76
Abstract: Specifies the hardware disk format, controller requirements and software handling of manufacturing site determined bad sectors of the RK06 and RK07 data cartridges and future disks. Conformance to this standard will result in improving reliability for the combined hardware/software system as experienced by our customers.

Title: DEC Representation of Data Values in ASCII Character Strings for Information Interchange Standard
DEC STD 145 Revision A Date: 27-May-76
Abstract: Defines the representation of data in character strings for interchange among DEC systems. It is an extension of ANSI X3.42. American National Standard for the Representation of Numeric Values in Characters Strings for Information Interchange.

Title: Standard Order for Front and Back Pages of Manuals
DEC STD 146 Revision B Date: 12-Jan-78
Abstract: Establishes sequence of pages preceding and following the text in a software or hardware manual. The required preliminary and back matter pages are listed; and each part of the preliminaries and back matter is defined.

Title: Digital Magnetic Tape Labels and File Structure Standard
DEC STD 149 Revision A Date: 18-Jan-79
Abstract: Defines four levels of magnetic tape label formats, record formats and tape mark relationships. Tapes written in conformance to this standard will also conform to American National Standard ANSI X.27-.1977, Magnetic Tape Labels and File Structure for Information Interchange.

Title: Punched Card Format: Requirements
DEC STD 151 Revision D Date: 06-Aug-81
Abstract: Defines two formats for encoding data on industry-compatible 80 column tabulating cards for the purpose of ensuring that such cards may be used as a compatible means of information interchange between Digital computer systems.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Error Logging Standard
DEC STD 153 Revision A Date: 26-May-77
Abstract: Describes the error logging system in terms of the data which should be captured into an error log file, the method of packaging the binary data into error log entries in the error log file, and the format necessary for compatible displays of the error log file.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: LSI-11 Bus Specification - Design Specification
DEC STD 160, Section 0 Revision A Date: 17-Sep-81
Abstract: This standard includes the information necessary to interface to the LSI-11 Bus, including the Q-Bus, which supports 16 and 18 bits of address space, and the Q22 Bus, which supports 16, 18, and 22 bits of address space.

Title: LSI-11 BUS Specification - History of the LSI-11 BUS
DEC STD 160, Section 1 Revision A Date: 17-Sep-81
Abstract: Describes earlier versions of the LSI-11 Bus for historical reference.

Title: Micrographics: Format and Quality Requirements for Microforms
DEC STD 162 Revision B Date: 03-Dec-81
Abstract: Describes the general format and quality requirements for each type of microform produced by Digital Equipment Corporation. The requirements are based on appropriate industry standards and U.S. Government specifications that have been adopted by the Digital Micrographics Committee.

Title: Software Use of the Graphic Character Set of ASCII
DEC STD 164 Revision A Date: 11-Jun-81
Abstract: Defines the subset of the ASCII graphic character set to be used by Digital software products.

Title: Standard for Documentation Symbology
DEC STD 165 Revision A Date: 21-Sep-78
Abstract: Defines character names, special key names, and notation conventions that are to be used in user documentation.

Title: Volume Identification for Removable Disk Pack Disk Systems
DEC STD 167 Revision A Date: 19-May-77
Abstract: Defines the format and location of the volume identification block required to allow disk packs of removable disk-pack systems to be identifed in all CPU families. This block will enable operating systems to identify the origin and format of volume and decide if the volume can be processed. This standard also defines a standard error message for volumes that can not be processed.

Title: PDP-11 Extended Instructions
DEC STD 168 Revision A Date: 18-Jan-79
Abstract: Provides architectural definition and control for PDP-11 instruction whose opcodes are in the reserved and extended opcode spaces.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: DEC Standard Coded Graphic Character Sets For Hardware and Software
DEC STD 169 Revision A Date: 29-May-82
Abstract: This standard specifies the DEC Multinational Character Set and Katakana. The graphic characters of ASCII, ANSI X3.4-1977, are a subset of the DEC Multinational Character Set. Also defines the alphabe[illegible]ations to be used with the DEC Multinational Character [illegible], the controls that may be used to support multiple graphic character sets, and the conversion between 7-bit and 8-bit environments.

Title: Standard for Documenting Systems Messages
DEC STD 170 Revision A Date: 28-Jul-77
Abstract: Every operation system will have a single manual describing all messages produced by all modules of the operating system. Unbundled software marketed by Digital will have a single message manual or a message section within its manual(s). Messages will be presented in alphabetical order with an explanation of the message, the severity of the error, the action that has been taken by the system, the recommended procedure to be taken by the system and the user, and the name of the module that produced the message.

Title: Legal Notices Required for Software Manuals and Licensed Software Sources
DEC STD 172 Revision B Date: 22-Dec-80
Abstract. Defines the legal notices to be printed in software manuals and to be coded into licensed software sources.

Title: Magnetic Tape Error Recovery Procedures for Read and Write Errors
DEC STD 174 Revision A Date: 18-Jan-79
Abstract: Defines the procedure and algorithms, including their sequence of execution to recover from operational read and write errors.

Title: Printed-Wiring Board Acceptance Criteria
DEC STD 176 Revision B Date: 20-Jul-81
Abstract: Specifies end-product criteria for rigid printed-wiring boards that have been fabricated or purchased for Digital Equipment.

Title: Digital Marking Standard
DEC STD 178 Revision A Date: 02-Feb-78
Abstract: Establishes the item marking requirements for identification of items produced by or for Digital Equipment Corporation.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Powder Metal Bearings and Bushings**
DEC STD 179, Section 1 Revision A Date: 11-May-78
Abstract: Provides the necessary information for the design engineer and/or manufacturing engineer to make an initial choice of powder metal bearings and bushings in cooperation with a powder metal parts supplier.

Title: **Powder Metal Structural Parts**
DEC STD 179, Section 2 Revision A Date: 11-May-78
Abstract: Provides the necessary information for the design engineer and/or manufacturing engineer to make an initial choice of powder metal structural parts.

Title: **Backplane and Wirewrap Module Release Process**
DEC STD 181 Revision B Date: 18-Mar-82
Abstract: Defines the process used for conversion of design information from an engineer's drawings into a released wirewrap data base and related soft tools necessary to build backplanes and/or wirewrap modules. Also describes the procedures for release, control, and distribution of wirewrap related information in the Engineering Documentation System.

Title: **Engineering Documentation Acceptance Criteria**
DEC STD 182 Revision B Date: 01-May-80
Abstract: Establishes the lettering requirements and relating drafting practices and procedures necessary to produce engineering drawings and documentation of a quality that is acceptable for microfilm and subsequent reproduction.

Title: **Archiving Microcode in the Engineering Documentation System**
DEC STD 183 Revision A Date: 08-Jun-78
Abstract: Describes the procedures and guidelines for release and control of Microcode Documentation that can be archived in the Engineering Documentation System.

Title: **ROM/PROM Documentation: Process and Requirements**
DEC STD 184 Revision A Date: 13-Sep-79
Abstract: Describes the procedures and requirements for development, release, and control of ROM/PROM documentation in the Engineering Documentation System.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Programmable Device Documentation: Process and Requirements**
DEC STD 184 Revision B(X01) Date: 15-JUL-82
Abstract: Describes the procedures and requirements for development, release, and control of programmable device documentation in the Engineering Documentation System.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: **Documentation of Computer Media in the Engineering Documentation System**
DEC STD 185 Revision E Date: 05-Nov-81
Abstract: Describes how to identify and control revision of computer media used in the design of products at Digital. Provides guidelines for structuring engineering information documented on magnetic media and lists standard file extensions for engineering documentation on magnetic media.

Title: **Signal Integrity**
DEC STD 186 Revision A Date: 09-Nov-78
Abstract: Describes how Digital systems should be designed, configured, and installed in order to maintain system signal integrity and thereby preserve funtionality and reliability.

Title: **Mechanical Fabrication Workmanship Standards**
DEC STD 187 Revision A Date: 05-Nov-81
Abstract: Specifies end-product criteria for fabricated metal or non-metal parts that have been manufactured by or purchased for Digital Equipment Corporation.

Title: **Archiving Engineering Information: Policy and Procedures**
DEC STD 188 Revision B Date: 20-Aug-81
Abstract: Digital policy and procedures for submitting engineering information to the Archive Administration are defined. Describes what should be submitted, who should submit it, and how information should be submitted.

Title: **Backplane Documentation Structure - Basic Requirements**
DEC STD 193 Revision A Date: 18-Mar-82
Abstract: Describes the documentation structure required to define, document and control engineering backplane design information.

Title: **SUDS Documentation Standard**
DEC STD 194 Revision A(X03) Date: 28-Sep-81
Abstract: Describes the requirements for identification, control, and release of SUDS generated documentation.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Legal Guidelines for Digital Publications**
DEC STD 197 Revision C Date: 27-May-82
Abstract: Defines legal guidelines for writing and reviewing major Digital publications for the purpose of controlling Digital proprietary information and protecting Digital against liability.

Table 1. Digital Standards, Listed In Numeric Order, With Abstracts (Continued)

Title: U.S. Government Export Controls and Export Licensing Requirements
DEC STD 198 Revision A Date: 07-Aug-80
Abstract: Describes the technical restrictions and export controls established by the U.S. Government that apply to Digital products. It specifies the technical calculations required to obtain licenses for the various types of products manufactured and sold by Digital.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Field Return of Defective Material: Inspection Criteria and Visual Inspection Procedures
DEC STD 264 Revision A Date: 05-Nov-81
Abstract: Establishes inspection criteria, methods, and procedures to be used by Digital Field Return/Repair Distribution Stockrooms. Defines criteria for determining if product should, or should not be returned for repair.

Title: Module Rework and Repair - Standard Procedures
DEC STD 265 Revision B Date: 07-Jan-82
Abstract: Establishes the standard module rework and repair methods and procedures to be used by manufacturing and the Field Service Module Repair Center.

Table 2. EL Class Manuals and Specifications (Continued)

Title: PC Board Layout Manual
Order No. ELEN312 Revision A Date: 15-Dec-81
Abstract: Contains printed circuit board layouts guidelines for the layout designer.

Title: Guide To PC Metrics
Order No. ELEN316 Revision A(X00) Date: 25-Jan-82
Abstract: Not available
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Digital-Developed Applicon Command Extensions
Order No. ELEN317 Revision A Date: 28-May-82
Abstract: The programs described in this manual were written at the Maynard facility of Digital Equipment Corporation. Almost all are extensions of the AGS/860 command set and were written using AGS/860 software.

Title: TWIGY User Guide:VERSION 1 of TWIGY
Order No. ELEN318 Revision A Date: 12-Apr-82
Abstract: This user's guide is intended for printed-circuit board layout designers who use the TWIGY routing program. This guide is intended for use with version 1 of the new TWIGY, which supersedes the old version of TWGY.

Title: TWIGY User Guide:VERSION 2 of TWIGY
Order No. ELEN318-02 Revision A(X00) Date: 24-Apr-82
Abstract: This user's guide is intended for printed-circuit board layout designers who use the TWIGY routing program. This guide is intended for use with version 2 of the new TWIGY, which supersedes the old version of TWGY.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: PDF User Manual, Version 1 Of PDF
Order No. ELEN319-00 Revision A(X00) Date: 30-Jun-82
Abstract: A user's guide to the Product Description File (PDF), which is a medium for transferring the product information from the engineering CAD design data bases to processes within manufacturing, which use this information.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: KPL-To-EPLS Process Manual
Order No. ELEN355 Revision A Date: 23-Feb-81
Abstract: Describes how to transfer a released automated parts list (K-PL) to the Engineering Product Library System (EPLS). Intended for use by site design library personnel in coordinating the KPL to EPLS process.

Table 2. EL Class Manuals and Specifications (Continued)

Title: **Phase Review Process Manual**
Order No. ELEN356 Revision A Date: 15-Jan-82
Abstract: Provides a consistent orientation for all product development teams towards the Phase Review Policy. The manual includes documents that are intended as guidelines and aids to assist project team members in defining terms and milestones referenced in the Phase Review Policy, DEC STD 028.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Applicon Hybrid Design Guide**
Order No. ELEN360 Revision A Date: 24-Dec-81
Abstract: Provides guidelines and requirements for the design, layout, artwork, and documentation of hybrids and their related substrates. Focused for individuals design Hybrid's using the Applicon computer aided design system in Maynard.

Title: **Unit Charge Reference Guide**
Order No. ELENCHG-RF Revision A Date: 02-Sep-81
Abstract: Guide for Engineering development organizations and their support groups. The purpose is to familiarize cost center personnel with the Unit Charge System. The groups referenced within this guide support Unit Charge as a project control tool that facilitates planning and control.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Unit Charge User's Guide**
Order No. ELENCHG-UG Revision A(X00) Date:
Abstract: This user's guide describes how to enter data into the Unit Charge system.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **CAD Engineering and Applications Handbook**
Order No. ELENCTS-HB Revision A(X02) Date: 27-Jan-82
Abstract: Describe the overall CAD organization, i.e. structure, function, processes, and requirements. Also contains informtion on how CAD tools are enhanced, released, and supported.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **EMI/RFI Mechanical Design Guide**
Order No. ELENEMI-UG Revision A(X00) Date: 15-Sep-81
Abstract: A guide to designing equipment enclosures and chassis that will minimize problems with electromagnetic and radio frequency interference and electrostatic discharge.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Table 2. EL Class Manuals and Specifications (Continued)

Title: **EPLS User's Manual**
Order No. ELENEPL-UG Revision B Date: 15-Jan-82
Abstract: Designed as a guide for anyone wanting to use the EPLS data base. EPLS contains product-related information i.e. parts, options, modules, parts lists, bills of materials (BOMs), Engineering Change Orders (ECOs) to parts, and what a part is used on.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Drafting Manual - Volume 1**
Order No. ELENGRS-01 Revision C Date: 15-May-82
Abstract: A collection of published standards, procedures and related information required for electrical/ mecnanical aspects of engineering documentation practice. (Part of a series being developed)

Title: **Drafting Manual - Volume 2**
Order No. ELENGRS-02 Revision C Date: 15-May-82
Abstract: A collection of published industry standards, procedures, and related information required for engineering design and documentation practice.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **Engineering Orientation Manual**
Order No. ELENGRS-OM Revision A Date: 01-Apr-82
Abstract: Familiarizes personnel with the organization and structure of the engineering development groups.

Title: **IDEA Training Manual**
Order No. ELENGRS-TM-IDEA Revision C Date: 06-Mar-81
Abstract: Compilation of information about the process and operation of the programs involved to complete a printed circuit board layout, from schematic input to clean space check, using the IDEA system.

Title: **Engineering Technical Training CAD Course Catalog 1981-82**
Order No. ELENGRS-UG-OCAD Revision B Date: 15-Apr-81
Abstract: Provides a listing and registration procedures for current courses in computer-aided printed circuit, electrical and mechanical design offered by Engineering Technical Training. These courses include SUDS, IDEA, Applicon and Unigraphics design drafting systems.

Title: **KPL User's Manual**
Order No. ELENGRS-UG-OKPL Revision C Date: 03-Aug-81
Abstract: Describes data and procedures for creating and maintaining an automated parts list in accordance with the requirements of the parts list standard, DEC STD 025.

Table 2. EL Class Manuals and Specifications (Continued)

Title: Magtape User's Guide
Order No. ELENMTP-UG Revision A(X06) Date: 15-Jan-82
Abstract: A guide for users of 1/2-inch magnetic tape. Describes general philosophy of the importance of careful handling, storing, cleaning, testing, shipping.
SE=E STATUS REPORT FOR CURRENT ACTIVITY

Title: PC Board Engineering Handbook
Order No. ELENPCB-00 Revision M Date: 17-May-82
Abstract: Compilation of drawings of standard Gerber features, finger arrangement, and layer construction configurations used in the engineering definition of printed circuit designs. References to these drawings appear on the MD drawing for modules defined in DEC STD 140.

Title: Electrical Design Guide For Printed Circuits
Order No. ELENPCD-TM Revision A Date: 10-Apr-81
Abstract: This guide provides methods and data to assist a circuit designer in determining what physical restrictions must be imposed on a PC layout and design, to guarantee acceptable electrical operation. The information provided is applicable to the TTL family logic on 34-layer circuit boards (two signal layers). Future volumes will cover other logic types and board configurations.

Title: Producibility Notebook
Order No. ELENPDQ-00 Revision D Date: 15-Apr-82
Abstract: A collection of published standards, procedures, and related information required to design printed wiring boards to take advantage of in-place processes and methods. Centers around DEC STD 030 and documents it's references. Focused at design engineers and individuals that support the design engineer.

Title: Quick Turnaround Process for Printed Circuit Design
Order No. ELENQTA UG Revision A Date: 10-Dec-81
Abstract: This document is a guide to the printed circuit layout design procedures that enable quick turnaround in printed circuit design. The procedures follow a 10 working days' schedule and are based on IDEA and other CAD programs. The designer does interactive on-line layout during the day, and runs programs requiring lengthy processing time, such as the TWGY router, in a batch mode overnight.

Title: SUDS Reference Card
Order No. ELENSDS-RF Revision A Date: 01-Nov-81
Abstract: A quick-reference to all SUDS commands

Table 2. EL Class Manuals and Specifications (Continued)

Title: **SUDS Training Manual**
Order No. ELENSDS-TM Revision A Date: 04-Feb-81
Abstract: Compilation of information about the SUDS process and operation of programs to enable an individual to create circuit schematics, wirelist design analysis files, and plot drawings, input files to other systems, and design macro's to maximize utilization of process parameters.

Title: **Wirewrap Process Manual**
Order No. ELENWPR-TM Revision A Date: 26-Mar-82
Abstract: Describes the overall wirewrap process, as well as specific wirewrap operator's tasks. Also outlines the data generation process and ECO procedure. Intended for use by wirewrap operator.

Title: **Wirewrap Program Manual**
Order No. ELENWWP-TM Revision A(X02) Date: 28-Sep-81
Abstract: Describe in detail the CAD soft tool programs used in the creation of wirewrap data base for backplanes and wirewrap modules. Intended for use by wirewrap operators.

Title: **Cost Manager's Guide For Manufacturing Part Number System**
Order No. ELMF012-05-USER Revision A Date: 02-Feb-80
Abstract: Describes the procedure for the installation of the manufacturing part number system described in DEC STD 012, Section 5, in a manufacturing plant.

Title: **Setting Labor Standards for Module Assembly and Test**
Order No. ELMF030-UG Revision A Date: 24-Nov-80
Abstract: This manual has been developed by Modules Process Management to describe how module assembly labor standards formulas are derived, how those formulas are to be used, and how labor standards are set for module test operations.

Title: **FF303 In-Circuit Tester Operator's Manual**
Order No. ELMF303-OP Revision A Date: 28-May-81
Abstract: Describes operating and maintenance procedure for the FF303 In-Circuit Tester.
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: **FF303 In-Circuit Tester, UUT Module Repair and Diagnostic Procedures**
Order No. ELMF303-RP Revision A Date: 31-Aug-81
Abstract: Provides basic information and procedures to repair modules that have been found to be faulty by the FF303 In-Circuit Tester. It can be used as a training manual by supervisors and repair personnel.

Table 2. EL Class Manuals and Specifications (Continued)

Title: PC Board Manufacturing Handbook, Volume 1
Order No. ELMFPCB-01 Revision Z Date: 21-Jun-82
Abstract: Compilation of drawings of Standard Assembly board, Process panel and Composite Artwork Standard Feature configurations used in specification of manufacturing requirements of Printed Circuit boards and panels. References to these drawings appear on the MD drawing for Modules defined in DEC STD 140.

Title: PC Board Manufacturing Handbook, Volume 2
Order No. ELMFPCB-02 Revision T Date: 17-Jun-82
Abstract: A continuation of ELMFPCB-01

Title: Product Reliability and Process Testing
Order No. ELMFPRT-00 Revision A Date: 01-Sep-75
Abstract: Describes a method developed by Central Reliability Engineering to improve the reliability of Digital products.

Title: Sequencer Operator's Manual
Order No. ELMFSEQ-OP-GEN3 Revision A Date: 01-Aug-80
Abstract: Describes operating procedures for the computer controlled, third generation component sequencer. Intended for use by supervisors and operators. Replaces A-SP-7665279-0-0 Axial Component Sequencer

Title: Rotary Sequencer Operator's Manual
Order No. ELMFSQ2-OP Revision A Date: 10-Jun-81
Abstract: Describes operating and maintenance procedures for the Rotary Sequencer.

Title: Wave Soldering and Aqueous Training Manual
Order No. ELMFTO1-TM Revision A Date: 10-Mar-81
Abstract: Consists of three learning modules and the related course materials and skills check lists used with the basic course to train wave soldering and aqueous cleaning system technicians.

Title: VCD Inserter Operator's Manual
Order No. ELMFVCD-OP-GEN3 Revision A Date: 01-Aug-80
Abstract: Describes the operating procedure for the Variable Center Distance (VCD), third generation Inserter. Intended for use by supervisors and operators. Replaces A-SP-7665281-0-0 VCD Component Insertion

Title: Satellite VCD Inserter Operator's Manual
Order No. ELMFVCD-OP-GEN4 Revision A Date: 02-Jan-81
Abstract: Describes the operating procedure for the Variable Center Distance (VCD) fourth generation Inserter. Intended for use by supervisors and operators.

Table 2. EL Class Manuals and Specifications (Continued)

Title: Solder Wave Machine and Aqueous Cleaner - Operator's Manual
Order No. ELMFWAV-OP-00AQ Revision A Date: 01-Aug-80
Abstract: Describes operating procedures for the Solder Wave Machine and Aqueous Cleaner System. Intended for use by process operators and supervisors.

Title: Gate Array Design Manual
Order No. EL1P400-UG Revision A Date: 29-Feb-80
Abstract: A detailed description of gate array technology and custom LSI circuits. Intended for users in engineering design groups who are not familiar with the rules and restrictions, as well as advantages, involved in using gate array technology.

Title: Design and Construction Guidelines For Computer Facilities
Order No. ELRECFE-UG Revision A Date: 26-Mar-32
Abstract: This guideline has been developed by RECO to provide effective computer systems in cost effective computer rooms.

Title: Applicon Plot File To Multiplot File Conversion Program
Order No. ELEN101-00 Revision A Date: 03-Nov-81
Abstract: Provides a description of the Applicon Plot File to Multiplot Conversion Program, its' theory of operation, and procedures to operate the program. A list of APF conventions, command formats, and common error messages are included.

Title: Multiplot File Format
Order No. ELEN375-00 Revision A Date: 12-Mar-82
Abstract: Lists Multiplot File Format requirements defined in CADEA software MULPLT version 98, dated 17-Sep-80.

Title: FCC Compliance Report
Order No. ELENFCC-RP Revision A(X00) Date: 12-Mar-82
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Global Equipment Plan
Order No. ELMFMPM-01 Revision A Date: 01-Jun-81
Abstract: Provides a detail description of the Global Equipment Plan, a plan implemented by the Modules Process Group in Acton, which acted as the central planner for the plants manufacturing equipment needs.

Title: Module Build Analysis System
Order No. ELMFMPM-02 Revision C Date: 01-Sep-80
Abstract: Provides volume metric information on various facets of Digital's Module Production. The Module Build Analysis (MBA) System extends unit profile information stored on each DEC Module to give volume data on such parameters as standard hours, material added, standard cost, board density, insertion device, and test instruments.

Table 2. EL Class Manuals and Specifications (Continued)

Title: Machine Capacity Models
Order No. ELMFMPM-03 Revision A Date: 01-Jun-81
Abstract: Provides a technical description of the following machine capacity models:
APST Capacity, DIP Inserter Capacity (24 Station), GR Capacity (General Radio Tester 1792), ST Capacity (Teradyne L427 Capacity and Cost), VCD Insertion Capacity (Revision #1), WS & Wave Solder, AQ and Aqueous Cleaning Process, ZT Capacity (Zehntel capacity and cost)

Title: ADL Competitive Metrics
Order No. ELMFMPM-04 Revision A Date: 01-Jun-81
Abstract: This specification provides a study undertaken by a contracted independent party (ADL, Arthur D. Little) which clearly gives detailed manufacturing operating comparisons between Digital and a selected sample of direct competitors. The competitive data complemented Digital's existing programs dealing with internal operating metrics at the plant and process level.

Title: Process Management Charter Package
Order No. ELMFMPM-05 Revision A Date: 01-Jun-81
Abstract: Provides a breakdown of the Modules Process Management group who had primary responsibility for the past operation and future direction of Digital's module manufacturing (assembly & test) processes: Process Engineering; Business Group; Process Equipment Training; and Modules Process Finance.

Title: D.L. (Direct Labor) Metrics Survey Results
Order No. ELMFMPM-06 Revision A Date: 01-Jun-81
Abstract: Provides a survey completed on some metrics related to D.L. (Direct Labor) on nine (9) of twelve (12) Digital's Modules Businesses. The figures given may be regarded as representative of the entire Module Business. The results show an average goal of 1555 hours per DL per year at no overtime, and utilizations and effectivity at 75%, with a plant potential of 1618 and 78% respectively (based on the best reported values). A list of identified "Module Process Goals" can be found on ADL Competitive Metrics #A-SP-ELMFMPM-04, (3.2 page 5).

Title: Metrics: FY'77 - FY'80
Order No. ELMFMPM-07 Revision A Date: 01-Jun-81
Abstract: Compiled by the modules interconnect process management group in Acton, MA. provides a metric breakdown of Digital's 12 modules lines. The specification gives a detailed analysis from FY'77 to budgeted FY'81 (e.g., Total Module Business; Module & Ratio Breakdown by Plant; Modules Plant Breakdown FY'80 Actual to FY'81 Budget).

Table 2. EL Class Manuals and Specifications (Continued)

Title: **Equipment Configuration Files On Global Assembly**
Order No. ELMFMPM-08 Revision A Date: 01-Jun-81
Abstract: Attached are Equipment Configurations on: APST Power Supply Tester, Cencorp Power Shear, Fairchild FF303 Test System, Genrad 1795 Test System, Hollis Astra Model 500 16" Wave Solder System, Hollis TDC-16A Wave Solder System, Stoelting, Hydro-Fleen III Aqueous Cleaner, Teradyne L417A "Shorts" Test System, Universal Multi-Module Dip Inserter, Universal Uni-Module Dip Inserter, Universal VCD Axial Component Inserter, Yoder Rotary Slitter, Zehntel TS400 Test System.

Title: **Module Process Management Manufacturing Training Video Tapes**
Order No. ELMFMPM-09 Revision A Date: 01-Jun-81
Abstract: A synopsis of Training Video Tapes froma Manufacturing Engineering Seminar held in Andover, MA in March, 1980. NOTE: There is a Video Cassette available with each synopsis.

Title: **Capacity Study On Mark V Hydraulic Shear**
Order No. ELMFMPM-10 Revision A Date: 01-Jun-81
Abstract: Documents in detail the results of a capacity study completed in the Westfield plant on the Mark "V" Hydraulic Shear. Using constraints, (e.g., down time, coffee breaks, etc.) the study displays a breakdown in determining available production labor hours on the insertion equipment.

Title: **Head Count Models**
Order No. ELMFMPM-11 Revision A Date: 01-Jun-81
Abstract: Provides the reader with an analysis of a Comparison Study on head Count Models. The study completed by Ron Bohlin, Chuck Kiezulas, Rich Powers, and Hank Rauch of the Modules Process Department in Acton, Ma. provides a breakdown (e.g.; Staffing, Financial, Production) on the formation of three (3) separate Module Business: 1) Stand Alone; 2) Plant Model; 3) In Plant.

Title: **Component Engineering Incoming Inspection Test Requirements And Methods Manual**
Order No. ELCE059-XX-XXXX Revision A(X00) Date:
Abstract: A compilation of PAVES requirements and individual Test Methods to supplement DEC STD 059, Section 1 on "PAVES Incoming Inspection Documentation Requirements".

Table 3. A-SP-7665XXX-X-X Specifications

Title: Wirewrapped Panel - Inspection Procedure
A-SP-7665001-00 Revision B Date: 01-Jul-76

Title: Alignment of Gold Contacts on Circuit Boards - Inspection Procedure
A-SP-7665002-00-0000-INIT Revision * Date: 18-Jul-68

Title: Solder Mask - Process Specification
A-SP-7665004-00-0000-INIT Revision * Date: 18-Jul-68

Title: Gold Plating - Process Specification
A-SP-7665005-00-0000-INIT Revision * Date: 18-Jul-68

Title: Solder Touch-up Specification
A-SP-7665010-00 Revision A Date: 06-May-74

Title: Mechanical Inspection for G610, G611, G612 - Inspection Procedure
A-SP-7665011-00-0000-INIT Revision * Date: 21-Aug-68

Title: Resistance Soldering - Process Specification
A-SP-7665012-00-0000-INIT Revision * Date: 07-Oct-68

Title: Wire-Wrap Process Specification and Inspection Procedure
A-SP-7665013-00 Revision F Date: 01-Aug-78

Title: G022 Cable Tester (Cable Type 70-05971)
A-SP-7665018-00-0000-INIT Revision * Date: 11-Dec-68

Title: Finish Specification - QC Procedure
A-SP-7665019-00-0000-INIT Revision * Date: 31-Dec-68

Title: Motor Balancing - Mfg. Standard
A-SP-7665020-00-0000-INIT Revision * Date: 02-Jan-69

Title: Chromicoat & Irridite Finish - Touch-up
A-SP-7665022-00 Revision A Date: 29-Dec-78

Title: Test Procedure Format for Power Supplies
A-SP-7665024-00-0000-INIT Revision * Date: 23-Jan-69

Title: Wire-Wrap Tooling Calibration QC Procedure
A-SP-7665027-00 Revision B Date: 20-May-82

Title: Layout Specification for Printed Circuit Back Panels
A-SP-7665028-00-0000-INIT Revision * Date: 17-Feb-69

Title: Procedure For Identifying Multiple Use Boards Without Handles
A-SP-7665029-00 Revision A Date: 05-Jun-74

Table 3. A-SP-7665XXX-X-X Specifications (Continued)

Title: Delay Timer (P. Sup. -P. Ctrl.) How to Connect it and How it Works
A-SP-7665030-00-0000-INIT Revision * Date: 07-Apr-79

Title: Power Supply - Control Model Acceptance - Procedure
A-SP-7665031-00-0000-INIT Revision * Date: 01-May-69

Title: DP01-A Cable Assy. - Mfg. Std.
A-SP-7665032-00-0000-INIT Revision * Date: 23-May-69

Title: DEC Semi-Automatic Wire Wrap Operations Manual
A-SP-7665033-00-0000-INIT Revision * Date: 06-May-69

Title: Repairing Damaged Connector Blocks and Backplane Assemblies
A-SP-7665034-00 Revision D Date: 01-Nov-74

Title: Diode & Transistor & Dual-In-Line Package (DIP) Replacement Charts
A-SP-7665035-00 Revision E Date: 01-Jul-75

Title: Requirements and Workmanship Standards for Power Supplies
A-SP-7665038-00 Revision A Date: 18-Jun-70

Title: Final Module Inspection Procedure
A-SP-7665039-00 Revision C Date: 01-May-74

Title: Procedure for Use of Module Inspection Gages
A-SP-7665042-00 Revision A Date: 08-Jun-73

Title: Wire Insulation Pull Test - QC Procedure
A-SP-7665047-00-0000-INIT Revision * Date: 03-Mar-70

Title: Memory Circuit Boards - Acceptance Standards
A-SP-7665052-00-0000-INIT Revision * Date: 03-Apr-70

Title: Standard Vibration Test On Flip Chip Systems
A-SP-7665057-00-0000-INIT Revision * Date: 20-May-70

Title: Control Of Fixtures Used In Fabrication Ships - QC Procedure
A-SP-7665060-00-0000-INIT Revision * Date: 14-Aug-70

➡ Title: Date-Coding Material
➡ A-SP-7665064-00 Revision C(X02) Date: 06-Jan-82
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Diode Forward Recovery Test Setup
A-SP-7665065-00-0000-INIT Revision * Date: 18-Nov-70

Title: Operating Instructions for Water Bath Thermal Shock & Drying Oven
A-SP-7665066-00 Revision D Date: 12-Sep-75

Table 3. A-SP-7665XXX-X-X Specifications (Continued)

Title: Corrective Action Request Procedure
A-SP-7665069-00 Revision A Date: 02-Jun-77

Title: Procedure For Processing Non-Conforming Material and In-Process Waiver
A-SP-7665075-00 Revision B Date: 01-Apr-78

Title: Procedure For ECO Status Sheet
A-SP-7665077-00-0000-INIT Revision * Date: 15-Dec-72

Title: XOR Testing Of PDP 11/45 Modules -- XOR Test Procedure
A-SP-7665089-00 Revision B Date: 01-Jun-73

Title: General Design Guide For Power Supplies and Power Controls
A-SP-7665095-00-0000-INIT Revision * Date: 05-Nov-70

Title: Acceptance Stamps - Use and Control Procedure
A-SP-7665096-00 Revision J Date: 06-Dec-79
SEE STATUS REPORT FOR CURRENT ACTIVITY

Title: Bus Splicing
A-SP-7665098-00-0000-INIT Revision * Date: 07-Dec-70

Title: Hardware Assembly Standard - QC Procedure
A-SP-7665099-00-0000-INIT Revision * Date: 08-Dec-70

Title: Cable Location Labeling
A-SP-7665111-00 Revision B Date: 15-May-81

Title: PDP8 Family Manufacturing Environmental Test Procedure
A-SP-7665114-00 Revision B Date: 01-Oct-73

Title: DK8-EA Acceptance Procedure
A-SP-7665126-00-0000-INIT Revision * Date: 01-Apr-71

Title: PC8E Acceptance Procedure
A-SP-7665129-00-0000-INIT Revision * Date: 23-Feb-71

Title: PC8-E Acceptance Procedure (Field)
A-SP-7665138-00-0000-INIT Revision * Date: 18-May-71

Title: Calibration, Maintenance, and Control Of Test and Measurement Equipment
A-SP-7665141-00 Revision D Date: 05-Aug-81

Title: W940 Inspection Procedure
A-SP-7665143-00 Revision D Date: 01-Nov-72

Title: W941 Inspection Procedure
A-SP-7665144-00 Revision D Date: 01-Nov-72

Table 3. A-SP-7665XXX-X-X Specifications (Continued)

Title: W943 Inspection Procedure
A-SP-7665146-00 Revision D Date: 01-Nov-72

Title: W951 Inspection Procedure
A-SP-7665147-00 Revision D Date: 01-Nov-72

Title: W951 Inspection Procedure
A-SP-7665148-00 Revision D Date: 01-Nov-72

Title: W952 Inspection Procedure
A-SP-7665149-00 Revision D Date: 16-Nov-72

Title: W953 Inspection Procedure
A-SP-7665150-00 Revision D Date: 01-Nov-72

Title: H734 AC Section Test Procedure For both 120V and 240V Models
A-SP-7665154-00-0000-INIT Revision * Date: 28-Sep-71

Title: XOR Module Test Station Specification
A-SP-7665155-00-0000-INIT Revision * Date: 06-Oct-71

Title: Electrical Test Procedure For G401 (Incoming)
A-SP-7665157-00-0000-INIT Revision * Date: 14-Sep-72

Title: Hollis TDC-16A Wave Soldering System: Process Control Requirements
A-SP-7665158-00 Revision D Date: 20-May-81

Title: Operating Specification For Twisted Wire Stripper
A-SP-7665159-00-0000-INIT Revision * Date: 10-Nov-71

Title: DEC Integrated Circuit Test System
A-SP-7665160-00-0000-INIT Revision * Date: 17-Nov-71

Title: KI-10 Ground Plane Process
A-SP-7665161-00-0000-INIT Revision * Date: 14-Mar-73

Title: Acceptance Test Procedure - Teradyne Pulse Parametric Test System S257S
A-SP-7665162-00-0000-INIT Revision * Date: 30-Nov-71

Title: Automatic Handler Attcahment For Teradyne S257S Test System
A-SP-7665163-00-0000-INIT Revision * Date: 29-Feb-72

Title: Teradyne J259/S257S Operating Procedure
A-SP-7665164-00-0000-INIT Revision * Date: 01-Mar-72

Title: Chromate Conversion Coating For Aluminum Alloys
A-SP-7665170-00 Revision A Date: 07-Mar-75

Title: Process Spec. For Iron Phosphate On Cold Roll Steel
A-SP-7665171-00-0000-INIT Revision * Date: 13-Nov-74

Table 3. A-SP-7665XXX-X-X Specifications (Continued)

Title: Emulsion Protection System
A-SP-7665178-00-0000-INIT Revision * Date: 30-May-73

Title: W900 Electrical Test Procedure (Incoming)
A-SP-7665179-00-0000-INIT Revision * Date: 12-Sep-72

Title: Operational and Maintenance Specification For Camera Back
A-SP-7665181-00-0000-INIT Revision * Date: 18-Aug-72

Title: Specification Of Liquid Medium Thermal Shock Chamber
A-SP-7665182-00-0000-INIT Revision * Date:?u 13-Oct-72

Title: Magnetic Tape Cleaning/Testing Procedure
A-SP-7665184-00-0000-INIT Revision * Date: 25-Apr-73

Title: Interfacing A Module To The 11/45 XOR Tester For Test - Steps To Take
A-SP-7665188-00-0000-INIT Revision * Date: 08-May-73

Title: Solder Resist Application
A-SP-7665189-00-0000-INIT Revision * Date: 15-May-73

Title: 11/40 XOR Test Procedure
A-SP-7665192-00-0000-INIT Revision * Date: 09-Jan-73

Title: 11/05 XOR Operation and Module Repair Procedure
A-SP-7665193-00-0000-INIT Revision * Date: 19-Dec-72

Title: Component Engineering Life Test System-Performance Specification
A-SP-7665196-00-0000-INIT Revision * Date: 23-Aug-73

Title: Process Specification For The Manufacture Of Pulse Transformer
A-SP-7665198-00-0000-INIT Revision * Date: 20-Oct-70

Title: Process Compatibility Test Methods
A-SP-7665212-00 Revision D Date: 22-Nov-77

Title: Installation and Operating Instructions For Automated Degreasers Model HL-600
A-SP-7665214-00-0000-INIT Revision * Date: 20-Dec-73

Title: General Radio 1732A Module Tester
A-SP-7665224-00 Revision B Date: 11-Sep-75

Title: Acceptance Procedure For GR1792A Test Systems
A-SP-7665224-01 Revision C Date: 01-Sep-75

Title. GR Module Test Program Request Procedure
A-SP-7665224-03-0000-INIT Revision * Date: 05-Nov-74

Table 3. A-SP-7665XXX-X-X Specifications (Continued)

Title: General Radio Module Program Generation Procedure
A-SP-7665224-04-0000-INIT Revision * Date: 05-Nov-74

Title: Generalized GR 1792A Start-Up and Operation Procedure
A-SP-7665224-05-0000-INIT Revision * Date: 08-Nov-74

Title: CAPS Diagnostic Message Interpretation
A-SP-7665224-06-0000-INIT Revision * Date: 08-Nov-74

Title: Module Repair Area
A-SP-7665224-07-0000-INIT Revision * Date: 15-Apr-75

Title: GR 1792A Logic Circuit Tester - Preventive Maintenance
A-SP-7665224-08-0000-INIT Revision * Date: 12-Nov-74

Title: Release Procedures For GR 1792A (CAPS V) Subassembly Diagnostics
A-SP-7665224-09 Revision A Date: 01-Oct-76

Title: General Radio 1792A Preventive Maintenance Procedure
A-SP-7665224-10-0000-INIT Revision * Date: 18-Feb-75

Title: Component Categories and Codes For Machine and Non-Machine Insertable Components
A-SP-7665228-00 Revision N Date: 17-Mar-82

Title: J384 System Specification
A-SP-7665230-00-0000-INIT Revision * Date: 15-Feb-74

Title: J384 Test Specification - Test Capability For DEC #21-10732-0-0
A-SP-7665230-01-0000-INIT Revision * Date: 26-Nov-74

Title: J384 Test Spec. - Test Procedure For DEC Part No. 19-11502-0-0
A-SP-7665230-02 Revision A Date: 01-Jan-75

Title: J384 Test [illegible]fication - Test Capability For DEC #21-11318-00, 01, 02
A-SP-7665230-03 Revision B Date: 01-Jun-75

Title: J384 Test Spec. - Test Procedure For DEC Part No. 19-11626-00-0
A-SP-7665230-04-0000-INIT Revision * Date: 11-Feb-75

Title: J384 Specification Test Procedure For DEC Part No. 19-12069-0-0
A-SP-7665230-05-0000-INIT Revision * Date: 23-Jan-75

Title: J384 Test Spec. - Test Procedure For DEC Part No. 19-11503-0-0
A-SP-7665230-06-0000-INIT Revision * Date: 23-Jan-75

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Title: J384 Spec. - Test Procedure For DEC Part No. 19-10818-0-0
A-SP-7665230-07-0000-INIT Revision * Date: 25-Feb-75

Title: J384 Test Specification - Test Capability For DEC #19-
A-SP-7665230-08-0000-INIT Revision * Date: 25-Apr-75

Title: J384 Test Specification - Test Capability For DEC #19-10396
A-SP-7665230-09-0000-INIT Revision * Date: 13-Aug-75

Title: J384 Test Specification - Test Capability For DEC 19-12459
A-SP-7665230-10-0000-INIT Revision * Date: 13-Aug-75

Title: J384 Test Spec. - Test Capability For DEC #23-XXYA1, A2, A8, A0, B1, B4 PROMS and ROMS
A-SP-7665230-11-0000-INIT Revision * Date: 13-Aug-75

Title: J384 Test Specification - Test Capability For DEC #21-12323-0-0
A-SP-7665230-12-0000-INIT Revision * Date: 21-May-75

Title: J384 Spec. - Test Procedure For DEC Part No. 19-126610-2
A-SP-7665230-13-0000-INIT Revision * Date: 13-Aug-75

Title: Silk Screen Artwork Procedure Process Specification
A-SP-7665233-00 Revision A Date: 08-Feb-79

Title: XOR Testing Of CMT Modules - Test Procedure
A-SP-7665234-00-0000-INIT Revision * Date: 10-Mar-74

Title: Printed Circuit Backplane Soldering Procedure/Process Specification
A-SP-7665236-00-0000-INIT Revision * Date: 27-Jun-74

Title: Miniature & Subminiature Incandescent Lamps
A-SP-7665251-00-0000-INIT Revision * Date: 28-Oct-74

Title: Handling and Soldering Requirements For 12-11670 Lead-Acid Battery
A-SP-7665252-00-0000-INIT Revision * Date:

Title: Resistor Flameproof Spec - Test BT34B
A-SP-7665253-00-0000-INIT Revision * Date: 26-Nov-74

Title: Use and Application Of The Actual Cost Jobs Closes Or Transfers
A-SP-7665254-00-0000-INIT Revision * Date: 11-Aug-75

Title: Process Requirements For AMPMODU MOD 1 Receptacles and Posts For Mated Modules
A-SP-7665258-00-0000-INIT Revision * Date: 13-Dec-74

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A-SP-7665260-00-0000-INIT Revision * Date: 04-Mar-75

Title: Process Spec. To Apply Vinyl Baked Enamel
A-SP-7665262-00-0000-INIT Revision * Date: 19-Nov-74

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A-SP-7665263-00-0000-INIT Revision * Date: 17-Jan-75

Title: 36-Inch Rotary Slitter - Operation Procedure
A-SP-7665263-01-0000-INIT Revision * Date: 17-Jan-75

Title: 36-Inch Rotary Slitter - Design Specification
A-SP-7665263-02-0000-INIT Revision * Date: 02-Aug-77

Title: Printed Circuit Board Gold Contact Cleaning - Process
A-SP-7665266-00-0000-INIT Revision * Date: 09-Jun-75

Title: Solderless Crimped Terminations - Controls and Procedures
A-SP-7665267-00 Revision E(X02) Date: 05-May-82
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Title: Solderless Crimped Terminations - Calibration
A-SP-7665267-01 Revision A(X00) Date: 08-Dec-81

Title: Process Maturity Test Specification
A-SP-7665268-00-0000-INIT Revision * Date: 15-May-75

Title: Design Maturity Test Specification
A-SP-7665268-01-0000-INIT Revision * Date: 14-May-75

Title: Continuity Test - Integrated Circuits
A-SP-7665269-00 Revision A Date: 30-Sep-75

Title: Sequencer Output Centering Requirements - Procedure
A-SP-7665270-00-0000-INIT Revision * Date: 30-Jun-75

Title: Cherry "Q" Rivet (Self Plugging) Procedure
A-SP-7665271-00-0000-INIT Revision * Date: 07-Jun-75

Title: Teradyne T317 Acceptance and Accuracy Test Procedure
A-SP-7665272-00-0000-INIT Revision * Date: 07-Jun-75

Title: Data I/O V Programming Capabilities For PROMS
A-SP-7665274-00-0000-INIT Revision * Date: 11-Sep-75

Title: S3260 Test Specification - Test Capabilities For TP1 Wafer
A-SP-7665275-00-0000-INIT Revision * Date: 25-Sep-75

Title: Operating Instructions For The Electroglas 1034 Wafer Prober
A-SP-7665276-00-0000-INIT Revision * Date: 22-Oct-75

Table 3. A-SP-7665XXX-X-X Specifications (Continued)

Title: Electrical Safety Product Test Procedure
A-SP-7665277-00 Revision D Date: 14-Jan-80

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Title: Machine Performance Data Collection Procedure
A-SP-7665278-00-0000-INIT Revision * Date: 11-Feb-76

Title: C/C Sequence Machine Preventative Maintenance - Procedure
A-SP-7665280-00-0000-INIT Revision * Date: 07-Jun-76

Title: C/C VDC Insertion Machine Preventative Maintenance Procedure
A-SP-7665282-00 Revision A Date: 19-Feb-79

Title: C/C DIP Inserter Preventative Maintenance Procedure
A-SP-7665284-00 Revision A Date: 19-Feb-79

Title: Fixed Head .01 Axial Lead Insertion Operation - Procedure
A-SP-7665285-00-0000-INIT Revision * Date: 29-Apr-76

Title: Fixed Head .01 Axial Lead Machine Adjustments & Preventive Maintenance - Procedure
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Title: Format Procedure For Writing Engineering Specifications
A-SP-7665287-00 Revision B Date: 25-Oct-80

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A-SP-7665289-00 Revision A Date: 31-Oct-80

Title: Aqueous Cleaning System Acceptance Procedure
A-SP-7665290-00 Revision B Date: 22-Apr-77

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Title: OP&L Distribution Lists
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Title: Printed Circuit Board Modules Cleaning Contamination
A-SP-7665299-00-0000-INIT Revision * Date: 13-Oct-76

Title: System Safety Grounding Procedure
A-SP-7665300-00-0000-INIT Revision * Date: 02-Oct-78

Title: System Safety Grounding Procedure
A-SP-7665300-00 Revision B(X00) Date: 02-Mar-82
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Title: Special Digital/DI-ACRO 24" Module Hand Shear - Operation Procedure
A-SP-7665301-00-0000-INIT Revision * Date: 16-Nov-76

Title: Abbreviated Hypot Test Procedure
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Title: Standard Module Defect Codes and Descriptions
A-SP-7665304-00-0000-INIT Revision * Date: 01-Sep-77

Title: Guidelines For Writing A Power Supply Test Requirements Specification
A-SP-7665305-00-0000-INIT Revision * Date: 26-Jan-79

Title: Loose Components Taping Machine Operation Procedure
A-SP-7665306-00-0000-INIT Revision * Date: 01-Apr-77

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Title: Purchase Specification Smog Hog Hollis Venting
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Table 3. A-SP-7665XXX-X-X Specifications (Continued)

Title: "PAVES" - Instructions For Completing "Transfer and Flow Form" (81-110-31)
A-SP-7665314-00 Revision B Date: 21-Feb-81

Title: Purchase Specification Transfer Conveyer
A-SP-7665315-00-0000-INIT Revision * Date: 19-May-79

Title: Solder Mask Requirements For H2D (Fine Line) Modules
A-SP-7665316-00 Revision A(X01) Date: 17-May-82
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Title: Cable/Harness Standard Times By Operation Codes
A-SP-7665317-00-0000-INIT Revision * Date: 17-Jun-76

Title: Vendor Calibration Facility Audit Checklist
A-SP-7665321-00-0000-INIT Revision * Date: 20-Sep-77

Title: Procedure For Handling Government Source Inspection Acceptance
A-SP-7665322-00-0000-INIT Revision * Date: 15-Sep-77

Title: Covernment Pre-Award Survey Procedure
A-SP-7665323-00-0000-INIT Revision * Date: 17-Aug-77

Title: Fast Mask Hollis Conveyorized Solder Machine
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A-SP-7665326-00 Revision B Date: 11-Mar-81

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A-SP-7665327-00 Revision C Date: 18-May-82

Title: Ship Cost Analysis
A-SP-7665327-00-0001-INIT Revision * Date: 02-Feb-78

Title: General Torque Requirements
A-SP-7665328-00 Revision B Date: 11-Dec-81

Title: Engineering Specification - General Torque Requirements
A-SP-7665328-00 Revision C(X00) Date: 10-Mar-82
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Title: Internal Product Quality Contract
A-SP-7665329-00 Revision A Date: 02-Apr-80

Title: Airvac Module Rework System - Process Operator Procedure
A-SP-7665330-00 Revision A Date: 10-Nov-80

Table 3. A-SP-7665XXX-X-X Specifications (Continued)

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A-SP-7665332-00-0000-INIT Revision * Date: 22-Mar-78

Title: Zinc Plating and Chromate Treatment: Inspection Procedure
A-SP-7665333-00 Revision A Date: 23-Jan-81

Title: Pace Desoldering System - Process Operator Procedures
A-SP-7665334-00 Revision A Date: 24-Nov-80

Title: Artos Cable Cutter: Operating Instructions
A-SP-7665335-00 Revision A Date: 01-Jan-81

Title: C/C BF/V.C.D. Component Insertion Operations - Procedure
A-SP-7665337-00-0000-INIT Revision * Date: 16-Nov-77

Title: Sequencer Centering Finger Assembly Alignment Procedure
A-SP-7665338-00 Revision B(X00) Date: 11-Feb-82
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Title: Cable/Harness Assembly Process Handbook
A-SP-7665339-00-0000-INIT Revision * Date: 30-Dec-77

Title: Vendor Material Deviation Request
A-SP-7665340-00-0000-INIT Revision * Date: 11-Oct-77

Title: Preliminary Documentation Postcard Procedure
A-SP-7665341-00-0000-INIT Revision * Date: 24-Jan-79

Title: Test Requirements For Equipment Released From Z Stock
A-SP-7665342-00-0000-INIT Revision * Date: 24-Jan-79

Title: Cable and Harness Identification Labels: Criteria and Application Methods
A-SP-7665343-00 Revision A Date: 02-Dec-80

Title: Instructions For Completing "Transfer and Flow Form"
A-SP-7665344-00 Revision A Date: 01-Jun-78

Title: Supplier Quality Survey - Metals Fabrication
A-SP-7665345-00 Revision A Date: 02-Jun-81

Title: Preliminary Purchasing Survey
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A-SP-7665345-02 Revision A Date: 02-Jun-81

Title: Quality Survey - General
A-SP-7665345-03 Revision A Date: 02-Jun-81

Title: Castings Quality Survey
A-SP-7665345-04 Revision A Date: 02-Jun-81

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A-SP-7665345-05 Revision A Date: 02-Jun-81

Title: Organic Coating Quality Survey
A-SP-7665345-06 Revision A Date: 02-Jun-81

Title: Plastics Quality Survey
A-SP-7665345-07 Revision A Date: 02-Jun-81

Title: Ship To Stock Supplier Quality Program - Audit Guideline For Plan Quality Engineer
A-SP-7665347-00 Revision A Date: 10-Dec-79

Title: Ship To Stock Supplier Quality Program - Basic Agreement and Release Procedure
A-SP-7665348-00 Revision A Date: 10-Dec-79

Title: Installation Audit Procedure
A-SP-7665349-00-0000-INIT Revision * Date: 26-Jul-78

Title: Systems Mfg. Final Product Audit Plan
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Title: Hydraulic Power Shear: System Specification and Acceptance Procedures
A-SP-7665351-00 Revision A Date: 10-Mar-81

Title: Populated Board Short Tester - System Description
A-SP-7665352-00 Revision A Date: 01-Apr-81

Title: Populated Board Short Tester - Operation
A-SP-7665352-01 Revision A Date: 01-Apr-81

Title: Populated Board Short Tester - Ordering Information
A-SP-7665352-02 Revision A Date: 01-Apr-81

Title: Sampling Plans for Ac Parametric Testing on SSI/MSI Devices
A-SP-7665357-00 Revision A(X02) Date: 01-Apr-81
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Title: Etch Repair Procedures
A-SP-7665362-00 Revision A Date: 30-Sep-81
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Title: Quality Plan For Procurement and Manufacture Of MSL Printed Wiring Boards
A-SP-7665363-00 Revision A(X00) Date:
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Table 3. A-SP-7665XXX-X-X Specifications (Continued)

Title: Pre-Qualification Survey and Qualification Survey For PWB Vendors
A-SP-7665364-00 Revision A(X00) Date: 21-Apr-81
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Title: FF303 In-Circuit Tester - Introduction Plan
A-SP-7665370-00 Revision A Date: 14-Sep-81

Title: FF303 In-Circuit Tester - Ordering Procedure
A-SP-7665370-01 Revision A Date: 14-Sep-81

Title: FF303 In-Circuit Tester - On-Site Acceptance
A-SP-7665370-02 Revision A Date: 14-Sep-81

Title: Incoming Inspection Procedures For Textured Plastic Components
A-SP-7665371-00 Revision A Date: 22-Mar-82

Title: Inner Layer Shorts Rework Procedure
A-SP-7665372-00 Revision A Date: 21-Dec-81
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Title: Source Inspection - Policy and Procedures
A-SP-7665373-00 Revision A Date: 10-Feb-82

Title: Solderability Specification For Process Printed Wiring Modules and Plated-Through Holes
A-SP-7665376-00 Revision A Date: 01-Feb-82

Title: Qualification Procedures For Digital Capacitors
A-SP-7665377-00 Revision A Date: 08-Jan-82

Title: Quality Audit Procedure: Systems Manufacturing Functional Product Audit-Plan
A-SP-7665378-00 Revision A Date: 28-Oct-81

Title: Hollis Astra Model 400 Wave Soldering System - Component Parts, Spare Parts, and Acceptance Procedure
A-SP-7665379-00 Revision A Date: 15-Dec-81

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A-SP7665380-00- Revision A(X01) Date: 17-May-82
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Title: FF333 In-Circuit Tester - Introduction Plan
A-SP-7665381-00 Revision A(X00) Date: 01-Jun-82
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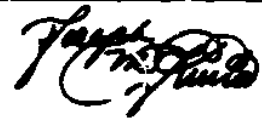
TITLE: INDEX/INFORMATION LOCATOR

ABSTRACT: This index has been prepared to help locate information contained in the DEC Standards, 7665 Specifications and American National Standards Institute (ANSI) standards under the control of DEC Standards Administration.

The DEC Standards, ANSI standards, etc. are arranged in an order believed to help the new employee find information.

Note

This is a PARTIAL index and is under development. It will be expanded as every update of this index occurs.

DATE	ECO#	ORIGINATOR	APPROVED	REV
27-Mar-81	Init	DEC Standards Administration		A

Any suggestions about additional subject titles and improvements to this index should be forwarded in writing to:

Joe Kurta
ML4-4/E99
DTN 223-8895

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A	MN	ELINDEX-01-0	A

SECTION 1 - INDEX/INFORMATION LOCATOR

TABLE OF CONTENTS/REVISION STATUS

1 INTRODUCTION

1.1 PURPOSE

This index/information locator is intended to help Digital employees find information that is contained in Digital Standards (DEC STDs), "7665" specifications, and American National Standards Institute (ANSI) standards that are administered by Digital Standards Administration.

1.2 SCOPE

The Digital Standards, ANSI standards, and other referenced information are organized in an order that is intended to help new employees find information.

This is a partial index that is intended to be continually updated and expanded. Any suggestions regarding additional subject titles, topics, or other improvements should be forwarded (in writing) to:

Joe Kurta Mgr.
Standards and Methods Information and Control

ML4-4/E99 DTN 223-8895

1.3 RESPONSIBILITIES

DEC Standards Administration is responsible for maintaining this index and keeping it complete and up to date.